

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A910A/E	Rev.	1.00
Title	Additional precaution on store buffer of RAM.		Information Category	Technical Notification		
Applicable Product	See below.	Lot No.	Reference Document	See below.		
		All lots				

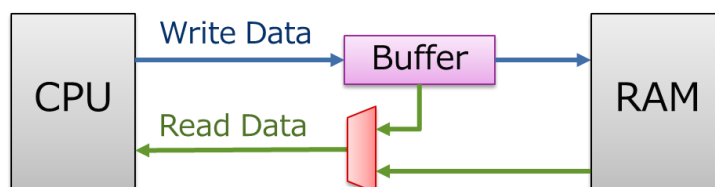
We added precaution to RAM section in User's Manual for Hardware for the applicable products stated below.

[Additional Description]

On path between RAM and CPU described follows, buffers are implemented to realize fast RAM access.

When a load instruction is executed from the same address after a store instruction to RAM, the load instruction may read out data from buffers instead of data on RAM.

Either of following procedures can be used to surely read data on Local RAM.



Take the following procedures to ensure to read out data from RAM:

First, a write instruction is executed for a RAM address different from one from which you really want to read out the data.

Then, read out the data from the RAM address of destination.

For SH72AW and SH72AY, this precaution applies only for the "RAM size 32K byte" setting.

Buffer is not used for the "RAM size 96K byte" setting.

[Applicable Products and Reference Documents]

Series	Group	Related documents	Rev.	Ref. No.
SH72AX	SH72AW	SH72AW Group, SH72AY Group User's Manual: Hardware	1.20	R01UH0145JJ0120
	SH72AY			

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