Plastic Packages for Integrated Circuits

Package Outline Drawing
P3.064
3 LEAD SMALL OUTLINE TRANSISTOR PLASTIC PACKAGE (SOT23-3)
Rev 3, 3/12

Dimensions are in millimeters. Dimensions in (    ) for Reference Only.

1. Dimensioning and tolerancing conform to AMSEY14.5m-1994.
2. Reference JEDEC TO-236.
3. Dimension does not include interlead flash or protrusions. Interlead flash or protrusions shall not exceed 0.25mm per side.
4. Footlength is measured at reference to gauge plane.

NOTES: