

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

Mask ROM number	
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**740 FAMILY MASK ROM CONFIRMATION FORM
SINGLE-CHIP MICROCOMPUTER M38503M2A-XXXSP/FP
RENESAS TECHNOLOGY**

Receipt	Date:	
	Section head signature	Supervisor signature

Note : Please fill in all items marked *.

* Customer	Company name	TEL ()	Issuance signature	Submitted by	Supervisor
	Date issued	Date:			

* 1. Confirmation

Specify the name of the product being ordered.
Three EPROMs are required for each pattern if this order is performed by EPROMs.
One floppy disk is required for each pattern if this order is performed by a floppy disk.

Microcomputer name: M38503M2A-XXXSP M38503M2A-XXXFP

Ordering by EPROMs

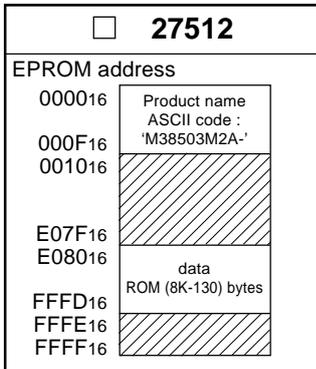
Specify the type of EPROMs submitted.
If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM

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 (hexadecimal notation)

EPROM type (indicate the type used)



In the address space of the microcomputer, the internal ROM area is from address E080₁₆ to FFFD₁₆. The reset vector is stored in addresses FFFC₁₆ and FFFD₁₆.

- (1) Set the data in the unused area (the shaded area of the diagram) to "FF₁₆".
- (2) The ASCII codes of the product name "M38503M2A-" must be entered in addresses 0000₁₆ to 0009₁₆. And set the data "FF₁₆" in addresses 000A₁₆ to 000F₁₆. The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address	'M' = 4D ₁₆	Address	'A' = 41 ₁₆
0000 ₁₆	'3' = 33 ₁₆	0008 ₁₆	'-' = 2D ₁₆
0001 ₁₆	'8' = 38 ₁₆	0009 ₁₆	FF ₁₆
0002 ₁₆	'5' = 35 ₁₆	000A ₁₆	FF ₁₆
0003 ₁₆	'0' = 30 ₁₆	000B ₁₆	FF ₁₆
0004 ₁₆	'3' = 33 ₁₆	000C ₁₆	FF ₁₆
0005 ₁₆	'M' = 4D ₁₆	000D ₁₆	FF ₁₆
0006 ₁₆	'2' = 32 ₁₆	000E ₁₆	FF ₁₆
0007 ₁₆		000F ₁₆	FF ₁₆

Mask ROM number	
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We recommend the use of the following pseudo-command to set the start address of the assembler source program because ASCII codes of the product name are written to addresses 0000₁₆ to 0009₁₆ of EPROM.

EPROM type	27512
The pseudo-command	*=△\$0000 .BYTE △'M38503M2A-'

Note : If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

Ordering by floppy disk

We will produce masks based on the mask files generated by the mask file generating utility. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this mask file. Thus, extreme care must be taken to verify the mask file in the submitted floppy disk.

The submitted floppy disk must be 3.5-inch 2HD type and DOS/V format. And the number of the mask files must be 1 in one floppy disk.

File code

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 (hexadecimal notation)

Mask file name

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 .MSK (equal or less than eight characters)

Note: When submitting data by floppy disk, do not write data to the product name area (addresses 0000₁₆ to 000F₁₆).
Write data to only ROM data area (addresses E080₁₆ to FFFD₁₆).

※ 2. Mark specification

Mark specification must be submitted using the correct form for the package being ordered. Fill out the appropriate mark specification form (42P4B for M38503M2A-XXXSP, 42P2R for M38503M2A-XXXFP) and attach it to the mask ROM confirmation form.

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※ 3. Usage conditions

Please answer the following questions about usage for use in our product inspection :

(1) How will you use the XIN-XOUT oscillator?

- Ceramic resonator Quartz crystal
 External clock input Other ()

At what frequency? $f(XIN) =$ MHz

(2) Which function will you use the pins P21/XCIN and P20/XCOUT as P21 and P20, or XCIN and XCOUT ?

- Ports P21 and P20 function XCIN and XCOUT function (external resonator)

(3) What is the power-supply voltage you will use?

$V_{CC} =$ V

(4) Which mode will you use for main clock division ratio? (You can check multiple boxes)

- high-speed mode
 middle-speed mode (except for the default mode after reset)
 low-speed mode

(5) Which function will you use?

- Timer A-D Converter
 Serial I/O1 WDT
 Serial I/O2 Pull up
 PWM

※ 4. Comments