To our customers,

## Old Company Name in Catalogs and Other Documents

On April $1^{\text {st }}, 2010$, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1 ${ }^{\text {st }}, 2010$
Renesas Electronics Corporation

## Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

## QzROM PROGRAMMING CONFIRMATION FORM SINGLE-CHIP 8-BIT MICROCOMPUTER M38827G5-XXXHP RENESAS TECHNOLOGY



Note : Please fill in all items marked $※$.

## ※1. Confirmation

Specify the name of the product being ordered.
The submitted floppy disk must be 3.5 -inch 2HD type and DOS/V format if this order is performed by a floppy disk. And the number of the mask files must be 1 in one floppy disk.

Microcomputer name:
M38827G5-XXXHP

File code
 (hexadecimal notation)

Mask file name

.MSK (equal or less than eight characters)
Note: Write data to only ROM data area (addresses B08016 to FFFD16).
ROM option data area: Addresses 1016

## Notes (RENESAS $\longrightarrow$ Customer)

1 : ROM data confirmation request
QzROM programming will be processed based on the mask file generated by the mask file generating utility. Only in case when ROM data programmed in the actual mass produced product differs from that of above mentioned mask file, RENESAS takes the responsibility. There is no Engineering Sample, thus please confirm the ROM data at the receipt of the Initial product delivery.
Should you find any problem, please return immediately. Two weeks without technical error feedback towards RENESAS will automatically be regarded as acceptance of products.

2 : ROM option ("MASK option" written in the mask file converter MM)
Either of the following data should be set to the ROM option data address $\left(10_{16}\right)$ of the mask file you have ordered. When you don't protect the ROM data, a third party can read out it.

When ROM data is protected $\quad 00_{16}$ Address $\mathbf{1 0}_{16}$

When ROM data is not protected $\quad \mathrm{FF}_{16}$ Address $10_{16}$
If you set except the above data or nothing at the ROM option data address $\left(10_{16}\right)$, We can't generate the ROM data. Then we request to submit the data again.

## 3 : Mark specification

You can appoint the mark by the mark specification form. Without submitting the mark specification form, your mark will be a standard mark. Please fill out the 80P6Q-A MARK SPECIFICATION FORM and attach it when you submit the QzROM PROGRAMMING CONFIRMATION FORM.

We can't deal with special font marking (customer's trademark etc.) in QzROM microcomputer.

## QzROM PROGRAMMING CONFIRMATION FORM

## SINGLE-CHIP 8-BIT MICROCOMPUTER

## M38827G5-XXXHP

## RENESAS TECHNOLOGY

※2. Usage conditions
For our reference of new products, please reply to the following questions about the usage of the products you ordered.
(1) Which operation source clock do you use?
$\square$ Ceramic resonator
$\square$ External clock input
At what frequency?Other ( )
(2) What is the voltage of power supply (Vcc) you use?

Typ. $=\square \vee \quad$ Min. $=\square \vee$
$\square$ V Min. $=$ $\square$ $V \quad$ Max. $=$ $\qquad$
(3) What is the ambient temperature you use?

Typ.= $\qquad$ C $\quad$ Min. $=$ $\qquad$ C Max. $=$ $\qquad$ C
(4) Which main clock (XIn-XOUT) division ratio mode will you use?In frequency/2 mode ( $\mathrm{f}(\phi)=\mathrm{f}(\mathrm{XIN}) / 2$ )In frequency/8 mode $(f(\phi)=f(X i N) / 8)$
(5) Do you use the Watchdog timer?
Use
$\square$ Not use
(6) Do you protect the ROM data?ProtectedNot protected

Thank you for cooperation.
※3. Comments

