RENESAS Tool News

RENESAS TOOL NEWS on December 1, 2013: 131201/tn4

Renesas Peripheral Driver Library for RX62N and RX621 Group Revised to V.1.10

We have revised Renesas Peripheral Driver Library for RX62N and RX621 Group from V.1.02 to V.1.10.
For the overview of the product, go to:
https://www.renesas.com/driver/rpdl
The above URL is one of our global sites.

1. Descriptions of Revision

1.1 Conformed to user's manual Rev.1.30 for RX62N and RX621 Group of MCUs

The library functions have been updated conforming to the specifications of the latest user's manual Rev.1.30.

1.2 Supported Endian Mode Added

Big endian mode is supported.

1.3 Options and functions added

(1) Addition of Parameter in Exception Handler Configuration Function

A parameter for the access exception handler has been added into the exception handler configuration function (R_INTC_CreateExceptionHandlers function).

(2) Addition of Continuous Receive Mode Option when using Serial Communications Interface (SCI) in asynchronous mode

The continuous receive mode option "Continuous receive Mode" has been added to the second argument of the SCI receive function (R_SCI_Receive function).

(3) Extension of Mode Compatible with SCI Format

The data transfer format options (MSB-first and LSB-first) of the second argument in the SCI creation function (R_SCI_Create function) have been applied to not only Smart card mode but other modes too.

(4) Addition of 10-bit Address Mode Option to I2C Bus Interface (RIIC) master send and master receive
The transmitting option "Slave address size override" for 10-bit address mode has been added to the second argument of the RIIC master send function (R_IIC_MasterSend function).
The transmitting option "Slave address size override" for 10-bit address mode has also been added to the second argument of the RIIC master receive function (R_IIC_MasterReceive function).

1.4 Other changes

For the other changes, refer to the Revision History in the Renesas Peripheral Driver Library User's Manual.

1.5 Problems fixed

1.5.1 The following ten problems have been fixed.

(1) With Using Callback Function for I2C Bus Interface (RIIC) to Send Slave Address
For details of the problem, see RENESAS TOOL NEWS Document No. 130701/tn6 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=130701tn6

(2) With Using bit 7 of Port G
For details of the problem, see RENESAS TOOL NEWS Document No. 130301/tn7 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn7

(3) With Using D/A Converter
For details of the problem, see RENESAS TOOL NEWS Document No. 130301/tn6 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn6

(4) With Using Serial Communication Interface (SCIa) of RX62N/RX621 Groups
For details of the problem, see RENESAS TOOL NEWS Document No. 130301/tn5 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn5

(5) With Using Real-Time Clock (RTC) of RX62N/RX621 Groups
For details of the problem, see RENESAS TOOL NEWS Document No. 130301/tn4 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn4

(6) With Using EXDMA Controller (EXDMAC) of RX62N/RX621 Groups For details of the problem, see RENESAS TOOL NEWS Document

No. 130301/tn3 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn3

(7) With Using I2C-Bus Interface (RIIC) of RX62N/RX621 Groups
For details of the problem, see RENESAS TOOL NEWS Document No. 130301/tn2 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn2

(8) With Setting Programmable Pulse Generators (PPGs)
For details of the problem, see RENESAS TOOL NEWS Document No. 120916/tn2 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=120916tn2

(9) With Serial Peripheral Interface (RSPI)
For details of the problem, see RENESAS TOOL NEWS Document No. 120601/tn7 at:
https://www.renesas.com/search/keyword-search.html#genre=document&q=120601tn7

(10) When Setting Multiplexed Pins of RX62N and RX621 Groups of MCUs
The problem that A19-B as an output signal to the external bus was disabled by calling function SCI creation function (R_SCI_Create) to create channel 5 after calling function BSC creation function (R_BSC_Create) has been solved. Corrections are made so that the creation function in each physical pin does not invalidate setting the physical sharing pin. Due to this, similar problems are also solved.

1.5.2 We are sorry that the following problem does not concern RX62N Group Renesas Peripheral Driver Library V.1.02.

Document No: 130401/tn2
With Making Changes to Alarm Settings of Real-Time Clock (RTC)
https://www.renesas.com/search/keyword-search.html#genre=document&q=130401tn2

For the Peripheral Driver Generator, we plan to fix the above problem in the next version.
Note that for the RX630 Group Renesas Peripheral Driver Library, this problem has already been fixed in V.1.10.
RX210 Group Renesas Peripheral Driver Library has also been fixed in V.2.00.

1.6 Library with Debugging Information Added

A library with debugging information has been added. Usage of this library enables the code of the Renesas Peripheral Driver to be

debugged on the source-code level.

2. Obtaining Library

Download the sample program of Renesas Peripheral Driver Library for RX62N and RX621 Group from the following Web page:
https://www.renesas.com/mw/rpdl_app_notes
Document Title: RX62N Group, RX621 Group Renesas Peripheral Driver Library

This sample program will be published on this Web page on December 5, 2013. The above URL is one of our global sites.
