

[Released on the Web]

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Rev.1.00

Jan. 16, 2017

Parameter Files for the PG-FP5 Flash Memory Programmer

Outline

We are revising several of the existing parameter files for the PG-FP5 flash memory programmer.

These parameter files are text files which contain the information required to use the PG-FP5 for writing to certain target MCUs. Refer to the URL below for an overview of the PG-FP5.

https://www.renesas.com/pg_fp5

1. Parameter Files to be Revised

1.1 Addition of Supported MCUs

- Parameter file PR5-RH850F1X for the RH850/F1x series will be revised from V1.06 to V1.07.
Added MCU: R7F701530 (RH850/F1H group)
- Parameter file PR5-RH850P1X for the RH850/P1x series will be revised from V1.03 to V1.04.
Added MCUs: R7F701372A (RH850/P1H-C group)
R7F701305 and R7F701323 (RH850/P1M group)
R7F701373A and R7F701374A (RH850/P1M-C group)

1.2 Modification to Parameter Files for the RX Family

- Parameter file PR5-R5F5110 for the RX110 group will be revised from V1.00 to V1.01 and the following parameter files for big endian will be also revised.

R5F5110H_B.pr5	V1.00 to V1.01
R5F5110J_B.pr5	V1.00 to V1.01
R5F51101_B.pr5	V1.00 to V1.01
R5F51103_B.pr5	V1.00 to V1.01
R5F51104_B.pr5	V1.00 to V1.01
R5F51105_B.pr5	V1.00 to V1.01

Applicable MCUs: R5F5110H, R5F5110J, R5F51101, R5F51103, R5F51104, and R5F51105

Details of the Correction: A phenomenon that a programming data becomes incorrect at writing a program file for big endian has been corrected.

- The following parameter files will be revised.
 - (a) Parameter file PR5-R5F564M for the RX64M group will be revised from V1.01 to V1.02.
Applicable MCUs: R5F564MF, R5F564MG, R5F564MJ, and R5F564ML
 - (b) Parameter file PR5-R5F5651 for the RX651 group will be revised from V1.00 to V1.01.
Applicable MCUs: R5F56514, R5F56517, and R5F56519
 - (c) Parameter file PR5-R5F565N for the RX65N group will be revised from V1.00 to V1.01.
Applicable MCUs: R5F565N4, R5F565N7, and R5F565N9
 - (d) Parameter file PR5-R5F571M for the RX71M group will be revised from V1.00 to V1.01.
Applicable MCUs: R5F571MF, R5F571MG, R5F571MJ, and R5F571ML

Details of the Correction of (a) to (d): The default settings of the [Mode pins at connection] area in the [Standard] tab in the Device Setup dialog box has been corrected. For a target system with a connector following the connection example with any of the emulators listed below, the PG-FP5 can be connected the target system without changing the default settings in the [Mode pins at connection] area.

- E1/E20 emulator
- E2 emulator Lite

2. Obtaining the Product

To obtain parameter files, download those you need from the download site below.

The above information will be available from January 20.

https://www.renesas.com/pg_fp5_download

Refer to chapter 3 of the PG-FP5 User's Manual: Common for details of how to install the file.

<https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut2922>

3. Required Firmware, FPGA Data, and Programming GUI

Obtain the release notes for the parameter files from the documentation page and consult them to confirm that you have the firmware, FPGA data, and programming GUI versions required to use the parameter files.

https://www.renesas.com/pg_fp5_documentation

Parameter files for PG-FP5 release note (Will be updated on January 20.)

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Jan. 16, 2017	-	First edition issued

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