

[Released on the Web]

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Rev.1.00

Apr. 1, 2017

Parameter File for the PG-FP5 Flash Memory Programmer

Outline

We will release a new parameter file and revise the existing one for the PG-FP5 flash memory programmer.

This parameter file is a text file that contains information on the target controller which is required to use the PG-FP5 for programming. Refer to the URL below for an overview of the PG-FP5.

https://www.renesas.com/pg_fp5

1. New Parameter File

- Parameter file for the RX24U group: PR5-R5F524U V1.00
Supported MCUs: R5F524UB, R5F524UC, and R5F524UE

2. Parameter File to be Revised

2.1 Addition of Supported MCUs

- Parameter file PR5-R5F524T for the RX24T group will be revised from V1.01 to V1.02.
Supported MCUs: R5F524TB, R5F524TC, and R5F524TE

3. Obtaining the File

Download a required parameter file from the following URL:

The files will be available from April 5.

https://www.renesas.com/pg_fp5_download

Refer to Chapter 3 of the PG-FP5 User's Manual: Common for details of how to install the file.

<https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut2922>

4. Required Firmware, FPGA Data, and Programming GUI

To confirm that you have the firmware, FPGA data, and programming GUI versions required to use the parameter file, see the release notes for the parameter file at the URL below.

https://www.renesas.com/pg_fp5_documentation

Parameter Files for PG-FP5 Release Note

This document will be available from April 5.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Apr. 1, 2017	-	First edition issued

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