

RENESAS TOOL NEWS on August 10, 2012: 120810/tn2

Parameter Files for Flash Memory Programmer PG-FP5 Released

We have published new parameter files as well as having revised several of the existing ones for flash memory programmer PG-FP5. These parameter files, written in text format, contain the information about MCUs that is necessary when you program the flash memories on those MCUs. For an overview of PG-FP5, go to:

https://www.renesas.com/pg_fp5

The above URL is one of our global sites.

1. Newly Published Parameter Files

The following parameter files have newly been published:

(1) For RX family

- PR5-R5F5210 V1.00 (for RX210 group)
Device MCU: R5F52105A, R5F52106A, R5F52107A, and R5F52108A
- PR5-R5F562G V1.00 (for RX62G group)
Device support: R5F562G7A, R5F562G7D, R5F562GAA, and R5F562GAD
- PR5-R5F5630 V1.00 (for RX630 group)
Device support: R5F56307, R5F56308, R5F5630D, and R5F5630E
- PR5-R5F5631 V1.00 (for RX631 group)
Device support: R5F5631A and R5F5631B
- PR5-R5F563N V1.00 (for RX63N group)
Device support: R5F563NA and R5F563NB

(2) For 78K family

- PR5-78F8077 V1.00 (for UPD78F8077)
Device support: UPD78F8071, UPD78F8072, UPD78F8073,
UPD78F8074, UPD78F8075, and UPD78F8077D

2 Revised Parameter Files

The following existing parameter files have been revised.

2.1 Increase in Device Support

(1) For V850 family

- PR5-70F4012 (for V850E2/Fx4) revised from V1.03 to V1.04.

Increased support: UPD70F3550, UPD70F4000, and UPD70F4001
(for V850E2/FG4)

UPD70F3551, UPD70F3553, and UPD70F3554
(for V850E2/FJ4)

UPD70F3556 (for V850E2/FK4)

(2) For RL78 family

- PR5-R5F109 (for RL78/F12 group) revised from V1.00 to V1.01.

Increased support: R5F1096A, R5F1096B, R5F1096C, R5F1096D,
R5F109AA, R5F109AB, R5F109AC, R5F109AD,
R5F109BA, R5F109BC, R5F109BD, R5F109GA,
R5F109GB, R5F109GC, R5F109GD, R5F109LA,
R5F109LB, R5F109LC, and R5F109LD

- PR5-R5F104 (for RL78/G14 group) revised from V1.03 to V1.04.

Increased support: R5F104AF, R5F104BF, R5F104CF, R5F104FF,
R5F104FG, R5F104FH, R5F104GF, R5F104GG,
R5F104GH, R5F104LF, R5F104LG, R5F104LH,
R5F104MF, R5F104MG, R5F104MH, R5F104PF,
R5F104PG, and R5F104PH

- PR5-R5F10R (for RL78/L12 group) revised from V1.00 to V1.01.

Increased support: R5F10RB8, R5F10RBA, R5F10RBC, R5F10RF8,
R5F10RFA, R5F10RFC, R5F10RG8, R5F10RGA,
R5F10RGC, R5F10RJ8, R5F10RJA, R5F10RJC,
and R5F10RLA

2.2 Inconsistency of Clock Frequencies

PR5-70F3509 (for V850E2/Px4, V850 family) revised from V1.01 to V1.03.

The reason is as follows:

Though the UPD70F3505A (V850E2/Px4) operates at a minimum of 48 MHz in multiplied CPU clock frequencies, it was expressed in the parameter file as 64 MHz. So this expression has been rectified to 48 MHz (8 MHz multiplied by 6) to remove inconsistency.

2.3 Reduction in Program-ROM Capacity

PR5-R8C3X (for R8C/3x series, R8C family) revised from V1.01 to V1.02.

The reason is as follows:

The program-ROM capacity of the R5F213MCQNNP (R8C/3MQ group) has been reduced from 128 KB to 112 KB in the MCU specifications. So the same reduction has been made in the parameter file.

For the changes made to the specifications of the R8C/3MQ group of MCUs and notes on using these MCUs, see Technical Update Document No. TN-R8C-A028A/E at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=TN-R8C-A028A/E>

3. Obtaining Parameter Files

Download the parameter files you need from the Web site at:

https://www.renesas.com/pg_fp5_download

The above-mentioned parameter files will be published on this site on August 10, 2012. For details of how to install them, see Chapter 3 of PG-FP5 Flash Memory Programmer User's Manual.

The above URL is one of our global sites.

4. Firmware Products, FPGA Data Items, and Programming GUIs

For the versions of the firmware products, FPGA data items, and programming GUIs necessary to use the above-mentioned parameter files, see the supplemental documents (in PDF) included with each parameter file.

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