

RENESAS TOOL NEWS on April 1, 2006: RSO-M30850T2-EPB-060401D

The Emulation Probe--M30850T2-EPB-- for the M32C/80, /84, /85, and /86 MCU Groups Released

We have released the emulation probe M30850T2-EPB. This probe is used in combination with the PC7501 emulator to emulate the M32C/80, /84, /85, and /86 groups, M16C family of MCUs.

1. Outline

The M30850T2-EPB is an emulation probe used in combination with the PC7501 emulator to support the M32C/80, /84, /85, and /86 groups, M16C family of MCUs.

It is the successor to the M30850T-EPB and supports the page-mode controlling function for the ROM-less versions of the M32C/84 MCU group (this function has not been supported by the M30850T-EPB).

NOTICE:

Since the M30850T2-EPB has been released, the production of the M30850T-EPB is discontinued. However we continue technical support of the M30850T-EPB.

2. The Contents of the Product Package

- (1) An MCU board--M30850T2-EPBM--for the M32C/80, /84, /85 and /86 groups
- (2) An evaluation MCU (already mounted)
- (3) A probe board--PCA7501EPBA--for the PC7501 emulator
- (4) A converter board--M30800T-PTC--for connecting a 100-pin 0.65-mm-pitch QFP package
- (5) A 100-pin 0.65-mm-pitch IC socket--IC61-1004-051--manufactured by Yamaichi Electronics Co., Ltd.
- (6) Two oscillator boards
 - o OSC-3 (for the 32-MHz main clock)

- OSC-2 (for main clocks)

(7) A user's manual

3. Specifications

The M30850T2-EPB operates under the following conditions:

- operating clock frequencies: 32 MHz maximum at 4.2--5.5 V supply voltage to target
- operating clock frequencies: 24 MHz maximum at 3.0--5.5 V supply voltage to target

For further information on the specifications, see its datasheets.

4. Precautions

The following precautions must be taken of when you use the probe for emulation. Note, however, that those are not applied to actual MCUs.

- (1) To operate the product at $VCC1 > VCC2$, supply a voltage of 3.3 V or higher to $VCC2$ of the target system.
- (2) If operating the product at $VCC1 > VCC2$ with the CPU clock frequency exceeding 30 MHz, set the SFR area wait states of the intelligent I/O register to 2 (that is, set bit PM13 to 1) when reading this area.
- (3) If the CPU reprogramming program is placed in the extended emulation memory, this program may run away. So, place the CPU reprogramming program in the internal RAM area or the external memory in the target system.
- (4) If you use the page-mode controlling function for the ROM-less versions of the M32C/84 MCU group, the following functions cannot operate properly at the write cycles where one word is written at a time into any area except emulation memory:
 - Real-time RAM monitor
 - Detecting of events
 - Displaying of real-time tracing

5. Ordering Information

If you place an order for the product, please supply the following items of information to your local Renesas Technology sales office or distributor:

Product Type: M30850T2-EPB

Type Name: M30850T2-EPB

For the price of the product, contact your local Renesas Technology sales office or distributor.

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