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DeviceUpdater (for High-performance Embedded Workshops Included with the SuperH RISC engine, H8SX, H8S and H8 Family Devices) Revised to V.1.02 Release 00

We have revised the DeviceUpdater (a tool for updating the High-performance Embedded Workshop's project files) from V.1.01 to V.1.02 Release 00.

1. High-performance Embedded Workshops Concerned

The High-performance Embedded Workshops included with the following products are concerned:

- (1) For the SuperH RISC engine family
 - C/C++ compiler package V.7 (Windows version)
 - C/C++ compiler package V.8 (Windows version)
 - C/C++ compiler package V.9 (Windows version)
- (2) For the H8SX, H8S and H8 families
 - C/C++ compiler package V.5 (Windows version)
 - C/C++ compiler package V.6 (Windows version)

2. Descriptions of Revision

- (1) Supported CPUs increased.
CPUs for which the High-performance Embedded Workshop can create projects have been increased.
- (2) I/O definition files updated.
Files that the High-performance Embedded Workshop automatically generates have been updated.
They include I/O register definition files, header files and so on.

For detailed information on items (1) and (2), see List of Updates on DeviceUpdater.

- (3) Introduced the function for fixing the following problem that is encountered in using a C/C++ compiler package V.9.00 Release 00 for the SuperH RISC engine family:

On creating projects for the SH7206 microprocessor

(During simulations of projects for the SH7206 microprocessor, simulations may be halted or memory access errors may arise because necessary memory resources for operand caches cannot be acquired.)

For details, see RENESAS TOOL NEWS "A Note on Using a C/C++ Compiler V.9.00 Release 00 for the SuperH RISC engine Family CPUs" issued on October 16, 2004.

3. **How to Revise Your Product (Free of Charge)**

Please download the revised DeviceUpdater from DeviceUpdater

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