[Notes]

RX Family

R20TS0872EJ0100 Rev.1.00 Sep. 01, 2022

Flash Module Using Firmware Integration Technology

RX Driver Package

Outline

When using the product in the title, note the following point.

- 1. Transition of Software Standby Mode for Flash Type 3 and Flash Type 4
- Transition of Software Standby Mode for Flash Type 3 and Flash Type 4
- 1.1 Applicable Products
 - (1) RX Family Flash Module Using Firmware Integration Technology (Flash Module) Rev.4.90 (document No. R01AN2184EJ0490) and earlier
 - (2) RX Driver Package

The following RX Driver Packages include the Flash Modules in (1).

Rev.1.36 (document No. R01AN6515EJ0136) and earlier

(3) FIT Modules used in combination with the Flash Modules and their application notes

The problem may occur when certain FIT Modules are used with the Flash Modules in (1).

Examples:

- RX Family Firmware Update Module Using Firmware Integration Technology (R01AN5824EJ) https://www.renesas.com/jp/en/search?keywords=R01AN5824
- RX Family TSIP (Trusted Secure IP) Module Firmware Integration Technology (R20AN0548EJ) https://www.renesas.com/jp/en/search?keywords=R20AN0548

1.2 Applicable Devices

- (1) Flash type 3: RX64M, RX660, RX66T, RX71M, and RX72T groups
- (2) Flash type 4: RX651, RX65N, RX66N, RX671, RX72M, and RX72N groups

1.3 **Details and Conditions**

After returning from software standby mode, if any of the following functions is executed without having performed "R FLASH Open" of the Flash Module, the return value contains an error (blocking mode) or the argument of the callback function contains an error (non-blocking mode).

"R_FLASH_Erase"

"R FLASH BlankCheck"

"R FLASH Write"

"R FLASH Control" with the lock bit program argument (FLASH CMD LOCKBIT WRITE) specified



1.4 Workaround

Take either of the following actions.

- (1) Before entering software standby mode, execute "R_FLASH_Close" of the Flash Module. After returning from the mode, execute "R_FLASH_Open".
- (2) In the following common functions in r_flash_rx\[\] src\[\] r_flash_fcu.c, enable or disable the flash write erase enable bit (FLWE) of the flash P/E protect register (FWEPROR).

Before modification

```
flash_err_t flash_reset(void)
{
  (Omitted)
    return FLASH_SUCCESS;
}

flash_err_t flash_pe_mode_enter(flash_type_t flash_type)
{
  (Omitted)
    return err;
}

flash_err_t flash_pe_mode_exit(void)
{
  (Omitted)
    return err;
}
```

After modification

```
flash_err_t flash_reset(void)
{
   (Omitted)
    FLASH.FWEPROR.BYTE = 0x00; /* FLWE bit is disabled */
    return FLASH_SUCCESS;
}

flash_err_t flash_pe_mode_enter(flash_type_t flash_type)
{
   (Omitted)
    FLASH.FWEPROR.BYTE = 0x01; /* FLWE bit is enabled */
    return err;
}

flash_err_t flash_pe_mode_exit(void)
{
   (Omitted)
   FLASH.FWEPROR.BYTE = 0x00; /* FLWE bit is disabled */
   return err;
}
```

1.5 Schedule for Fixing the Problem

The problem will be fixed in the next version.

Revision History

		Description	
Rev.	Date	Page	Summary
1.00	Sep.01.22	-	First edition issued

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