[Notes]
PG-FP6 Flash Memory Programmer
(RA4M2, RA4M3, RA6M4, and RA6M5 Groups)

Overview

When using the PG-FP6 flash memory programmer, take note of the following point.

1. Notes on RA4M2, RA4M3, RA6M4, and RA6M5 groups

1. Notes on RA4M2, RA4M3, RA6M4, and RA6M5 groups

1.1 Applicable Products

Programming GUI for the PG-FP6 "FP6 Terminal" V1.05.00, V1.05.01, and V1.05.02

1.2 Applicable Devices

RA4M2 group of the RA family: R7FA4M2AB, R7FA4M2AC, and R7FA4M2AD

RA4M3 group of the RA family: R7FA4M3AD, R7FA4M3AE, and R7FA4M3AF

RA6M4 group of the RA family: R7FA6M4AD, R7FA6M4AE, and R7FA6M4AF

RA6M5 group of the RA family: R7FA6M5AG, R7FA6M5AH, R7FA6M5BF, R7FA6M5BG, and R7FA6M5BH

1.3 Description

When a command is run, the I/O pin of PG-FP6 is not able to set the MD pin of an applicable device to Low causing the following errors, and the product cannot be used.

When V1.05.00 or V1.05.01 is used

➢ Console window of Programming GUI for the PG-FP6
ERROR(E012): Connection or Synchronisation failed.

➢ Message display of the PG-FP6 main body
ERROR: 012 Synchron. failed

When V1.05.02 is used

➢ Console window of Programming GUI for the PG-FP6
ERROR(E004): The device is not responding.

➢ Message display of the PG-FP6 main body
ERROR: 004 No response

1.4 Workarounds

Use the target system to set the MD pin to Low. For a detailed circuit example, see 11.19 RA (UART communications) in the PG-FP6 Flash Memory Programmer User's Manual (Document number R20UT4469EJ0500) from the following URL.

https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut4469

1.5 Permanent Measure

This problem will be fixed in Programming GUI for the PG-FP6 "FP6 Terminal" V1.05.03. The version will be available from July 2021.

Revision History

Rev.	Date	Description	
		Page	**Summary**
1.00	Jun.01.21	-	First edition issued

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