

[Notes]

R20TS0436EJ0100

Rev.1.00

Jun. 16, 2019

## e<sup>2</sup> studio Smart Configurator Plug-in, Smart Configurator for RX

### Outline

When using the e<sup>2</sup> studio Smart Configurator Plug-in and Smart Configurator for RX, note the following point.

1. When using general PWM timer

### 1. When Using General PWM Timer

#### 1.1 Applicable Products

- e<sup>2</sup> studio V7.2.0 (Smart Configurator Plug-in V1.5.0) or later
- Smart Configurator for RX V1.5.0 or later

#### 1.2 Applicable Devices

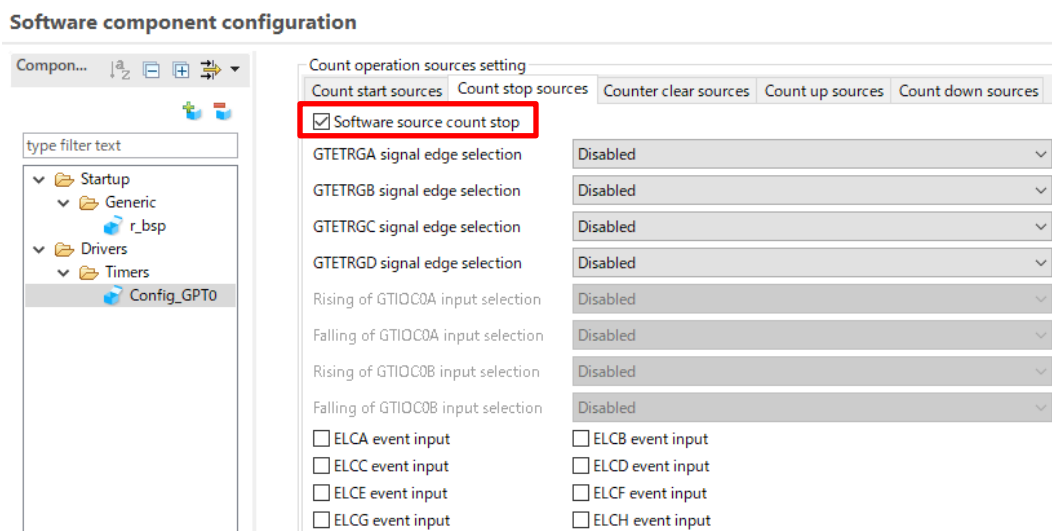
- RX family:  
RX66T and RX72T groups

#### 1.3 Details

When the software source count stop is selected as the count stop source for the general PWM timer, the timer counter does not stop because the setting value in the general PWM timer software stop register (GTSTP) is incorrect.

#### 1.4 Conditions

The error occurs when software source is selected as the count stop source for General PWM Timer component.



## 1.5 Workaround

Change the setting value in the general PWM timer software stop register (GTSTP) to “1” in the source file below.

Note: If you perform code generation again, the setting value will be reset to the state before the change. You will need to modify the source file after each code generation.

- *Source file:* “<General PWM Timer configuration name>.c”
- *Function:* “void R\_<General PWM Timer configuration name>\_Stop(void)”

The <General PWM Timer configuration name> varies depending on the component of general PWM timer selected.

A modification example for RX66T when <General PWM Timer configuration name> is Config\_GPT0 (initial value) is shown below. Modification is shown in red.

Before modification:

```

/*****
* Function Name: R_Config_GPT0_Create
* Description  : This function stops the GPT0 channel counter
* Arguments    : None
* Return Value : None
*****/
void R_Config_GPT0_Stop(void)
{
    /* Disable GPTW0 start write protect */
    GPTW0.GTWP.BIT.STPWP = 0U;

    /* Stop GPTW0 counting */
    GPTW0.GTSTP.BIT.CSTOP0 = 0U;

    /* Enable GPTW0 start write protect */
    GPTW0.GTWP.BIT.STPWP = 1U;
}

```

After modification:

```

/*****
* Function Name: R_Config_GPT0_Create
* Description  : This function stops the GPT0 channel counter
* Arguments    : None
* Return Value : None
*****/
void R_Config_GPT0_Stop(void)
{
    /* Disable GPTW0 start write protect */
    GPTW0.GTWP.BIT.STPWP = 0U;

    /* Stop GPTW0 counting */
    GPTW0.GTSTP.BIT.CSTOP0 = 1U;

    /* Enable GPTW0 start write protect */
    GPTW0.GTWP.BIT.STPWP = 1U;
}

```

## 1.6 Schedule for Fixing the Problem

This problem will be fixed in the next version. (Scheduled to be released in July 2019.)

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Jun.16.19	-	First edition issued

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