Outline

When using the products in the title, note the following point.
1. When using timer RD for controlling the event count period for timer RJ in event counter mode

1. When Using Timer RD for Controlling the Event Count Period for Timer RJ in Event Counter Mode

1.1 Applicable Products

- e² studio Simulator for RL78/G14 V1.00.00 (e² studio V7.0.0) or later
- CS+ Simulator for RL78/G14 V1.00.00 (CS+ for CC V7.00.00) or later

1.2 Applicable Devices

RL78 family: RL78/G14 group

1.3 Details and Conditions

When using timer RJ in event counter mode, if the settings of the related registers meet the following (1) to (3), the simulator operates differently from device specifications.

1. Timer RJ mode register 0 (TRJMR0)
   - Timer RJ operating mode selection: Event counter mode

2. Timer RJ I/O control register 0 (TRJIOC0)
   - TRJIO count control: Event is counted during polarity period (high-level period or low-level period) specified for timer output signal

3. Timer RJ event pin select register 0 (TRJISR0)
   - Timer output signal selection: “TRDIOD1” or “TRDIOC1” to select timer RD

- Simulator’s operations that differ from device specifications
  - When “an event is counted during the high-level period” is selected as polarity selection, the TRJ0 counter does not count, even if the output signal of timer RD is in the high-level period.
  - When “an event is counted during the low-level period” is selected as polarity selection, the TRJ0 counter counts, even if the output signal of timer RD is in the high-level period.

1.4 Workaround

There is no workaround.
1.5 Solution

This problem will be fixed in the next version update in July 2020.
Revision History

<table>
<thead>
<tr>
<th>Rev.</th>
<th>Date</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.00</td>
<td>Mar.16.20</td>
<td>First edition issued</td>
</tr>
</tbody>
</table>

Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.

The past news contents have been based on information at the time of publication. Now changed or invalid information may be included.

The URLs in the Tool News also may be subject to change or become invalid without prior notice.

Corporate Headquarters
TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Contact information
For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:
www.renesas.com/contact/

Trademarks
Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.