

[Notes]

R20TS0398EJ0100

Rev.1.0

Feb. 01, 2019

# e<sup>2</sup> studio Code Generator Plug-in, AP4 Coding Assistance Tool for RZ

## Outline

When using the products in the title, note the following point:

1. Point for caution regarding the write value of RSPI status register (SPSR)

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### 1.1 Applicable Products

- e<sup>2</sup> studio V4.0.0.26 (Code Generator Plug-in V2.0.0 and later)
- AP4 coding assistance tool for RZ V1.00.00

### 1.2 Applicable MCUs

- RZ family: RZ/T1 group

### 1.3 Details

When using the serial peripheral interface (RSPIa), unintended operation may occur due to an incorrect value set to the RSPI status register (SPSR).

For details, refer to the technical updates of the applicable MCU.

[https://www.renesas.com/search/keyword-search.html#genre=document&q=TN-RZ\\*-A054A](https://www.renesas.com/search/keyword-search.html#genre=document&q=TN-RZ*-A054A)

RZ/T1Group User's Manual:

Restriction on the write value of the RSPI status register

Although the “void R\_RSPIO\_Start(void)” function in source file “r\_cg\_rspi.c” output by the applicable products sets the reserved bits of the SPSR register to 0, it has no effect on the operation as it is written when the SPE bit of the RSPI control register (SPCR) is 0 (RSPI function disabled).

## Revision History

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