

Note on Using Renesas Peripheral Driver Library for RX62N/RX621 Groups --With Using bit 7 of Port G--

When using Renesas Peripheral Driver Library for the RX62N/RX621 groups, take note of the following problem:

- With using bit 7 of Port G

1. Product Concerned

- RX62N, RX621 Group Renesas Peripheral Driver Library V.1.02

2. Description

When using the library functions for I/O port of the product concerned if you manipulate bit 7 of Port G and the others (bits 0-6) simultaneously, False is inevitably returned as the return value. As a result, the manipulation is disabled.

Example:

```
-----  
/* RPDL definitions */  
#include "r_pdl_io_port.h"  
/* RPDL device-specific definitions */  
#include "r_pdl_definitions.h"  
  
void func(void)  
{  
    uint8_t output;  
  
    /* Read the output type */  
    R_IO_PORT_ReadControl(  
        // Bit 7 of Port G and the other bits manipulated  
        // simultaneously  
        PDL_IO_PORT_G_0 | PDL_IO_PORT_G_7,  
        PDL_IO_PORT_TYPE
```

```
    &output  
);  
}
```

3. Workaround

Do not manipulate bit 7 of Port G and the others (bits 0-6) simultaneously. You should manipulate bit 7 of Port G independently. If you manipulate them at a time, set the value to the I/O register for the port directly.

4. Schedule of Fixing Problem

We plan to fix this problem in a version published later.

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