

Note on Using Real-Time OSes RI600PX and RI600/PX for RX Family

When using the real-time OSes for the RX family of MCUs RI600PX and RI600/PX, take note of the following problem:

- With setting of the task context register
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1. Products and Versions Concerned

- (1) RI600PX V1.01.00 through V1.01.01 (managed by CubeSuite+)
- (2) RI600/PX V.1.00 Release 00 through V.1.00 Release 01
(managed by High-performance Embedded Workshop)

2. Description

When the setting of the task context register corresponds to any of the following value or sets of values at the time of configuration, 0x100 is not set as the initial value of the FPSW (floating-point status word register).

- FPSW
- FPSW,ACC
- MIN,FPSW
- MIN,FPSW,ACC

When a value other than those indicated above is set, this problem does not arise as the value of the FPSW is not used.

3. Workaround

Use the `set_fpsw` function to insert code which sets 0x100 as the value of the FPSW at the head of the task.

4. Schedule for Fixing the Problem

- (1) RI600PX

This problem will be fixed in the next version (scheduled for release on March 24, 2014).

Note that in the next version of RI600PX, instead of the specification for 0x100 to be set as the initial value, the specification is for a suitable value to be set by a floating-point operation related option of the compiler.

(2) RI600/PX

There is no plan to fix this problem.

Use the method described in Section 3 to avoid this problem.

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