RENESAS Tool News

Note on Using Peripheral Driver Generator V.2.08 and V.2.09

When using the Peripheral Driver Generator, take note of the following problem:

- With using code generated for the Serial Peripheral Interface (RSPI) in the RX62N group of MCUs

1. Product and Versions Concerned

Peripheral Driver Generator V.2.08 or V.2.09

2. Description

When either of the modes below is selected in the Mode settings area, the RSPI for the RX62N group of MCUs is set, and source code is generated, the output source code includes undefined macros (see NOTE below) causing an error at the time of compilation of the generated source code.
- Clock synchronous operation (three-wire method) master mode
- Clock synchronous operation (three-wire method) slave mode

NOTE:
 The undefined macro is either "PDL_SCI_MODE_SYNC_MASTER" or "PDL_SCI_MODE_SYNC_SLAVE."

3. Workaround

Overwrite the "r_pdl_spi.h" file (see NOTE below) stored in the location where Peripheral Driver Generator is installed with a new file.

The new file, tn_140701_PDG_patch.zip (1.68 KB), can be obtained here; download the zip file and decompress it.
This site will be opened on July 22, 2014.

NOTE:
 The file is installed in the following folder by default:

C:¥renesas¥PDG2¥source¥RX¥RX62N¥i_src

4. Schedule for Fixing the Problem

This problem will be fixed in a later revision of the product.