RENESAS Tool News

Note on Using Peripheral Driver Generator

When using Peripheral Driver Generator V2, take note of the following problem:

- With entering the information of the external bus of the RX62N group of MCUs

1. Product and Versions Concerned

Peripheral Driver Generator V.2.01 and later

2. Description

If the information of the external bus of the RX62N group of MCUs is entered in the product, the data displayed in the pin-function pane (the Pin function and Peripheral pin usage sheets) may be different from the entered one.

3. Conditions

This problem arises if either of the following conditions is satisfied:
(1) As data items of the external bus, the SDRAM area is specified but the CS area is not.

In this case, the pin-function pane shows that the WR# and WR0# pins are disabled. And if you assign other output functions to these pins, the warning message saying that conflictions occur does not appear.

However, the output functions you have assigned to them are not performed.
(2) In the settings of address output pins of the external bus, the A0 and A1 pins are disabled.

In this case, the pin-function pane shows that the following pins
are enabled when the following conditions are satisfied respectively.
However, they do not function when the A0 and A1 pins
are disabled:

BC0#:   When the CS area is specified, and a single-write
          strobe is selected from the list of write access
          modes

DQM2 and DQM3: When the SDRAM area is specified, and the 32-bit
          bus length is selected.

4. Workarounds

For condition (1):
  Do not assign other functions to the WR# and WR0# pins. When using
  the external bus, you cannot use these pins for other output
  functions.

For condition (2):
  If you use the BC0#, DQM2, or DQM3 pin, enable the A0 and A1 pins
  in the settings of address output pins.

5. Schedule of Fixing Problem

We are going to fix this problem at a later revision of the product.