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A Note on Using the High-performance Embedded Workshop V.4.00.00 --On Specifying Address Parameters in HDI Commands--

Please take note of the following problem in using the High-performance Embedded Workshop V.4.00.00, an integrated development environment:

- On specifying address parameters in HDI* commands
* Hitachi Debugging Interface

1. Product and Version Concerned

The High-performance Embedded Workshop V.4.00.00

To check for the version number of your High-performance Embedded Workshop, open the Help menu and select the About High-performance Embedded Workshop command.

2. Description

If the address parameters of the HDI commands hdi assemble, hdi memread, and hdi memwrite are set to 0x80000000 or greater in a CPU with 32-bit memory space, the problems explained below arise.

(1) In the hdi assemble command

Command syntax: hdi assemble <address> <instruction>

Problem: If the address parameter is set to 0x80000000 or greater, the result of assembling the instruction parameter cannot be provided.

Example: hdi assemble 0x80000000 NOP

(2) In the hdi memread command

Command syntax: hdi memread <space> <address> <count> <size>

Problem: If the address parameter is set to 0x80000000 or greater,

data cannot be read from memory.

Example: hdi memread 0 0x80000000 1 1

(3) In the hdi memwrite command

Command syntax: hdi memwrite <space> <address> <size> <data>

Problem: If the address parameter is set to 0x80000000 or greater,
data cannot be written into memory.

Example: hdi memwrite 0 0x80000000 1 1

3. Workaround

Don't set the address parameters of the HDI commands hdi assemble, hdi memread, and hdi memwrite to 0x80000000 or greater in a CPU with 32-bit memory space.

4. Schedule of Fixing the Problem

We plan to fix this problem in our next release of the product.

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