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Note on Using the Following Tools: CS+ Code Generator for RL78 (CS+ for CC), CS+ Code Generator for RL78 (CS+ for CA and CX), e² studio Code Generator Plug-in, Applilet3 Coding Assistance Tool for RL78

When using the CS+ Code Generator for RL78 (CS+ for CC), the CS+ Code Generator for RL78 (CS+ for CA and CX), the e² studio (Code Generator Plug-in), or the Applilet3 coding assistance tool for RL78, take note of the problem on the following point that is described in this note.

- Pin settings for the IICA serial interface when setting the PIOR to change the assignment of pin functions
Applicable MCUs: RL78/G12 group (20- and 24-pin products)

1. Applicable Products

- V2.03.00 and later versions of the CS+ Code Generator for RL78 (CS+ for CC)
- V2.03.00 and later versions of the CS+ Code Generator for RL78 (CS+ for CA, CX)
- V2.1.0.21 and later versions of the e² studio (V1.0.0 and later versions of the Code Generator Plug-in)
- V1.00.00 and later versions of the Applilet3 for RL78

2. Applicable MCUs

- RL78/G12 group (20- and 24-pin products)

3. Description

When the IICA pin functions are redirected in the following way by setting the peripheral I/O redirection register (PIOR) to change the pin assignment, the generated code for the pin setting will have an error. This makes the IICA clock and data pins unusable with this setting.

- (1) In setting the arrangement of pins for the clock generation circuit, select peripheral I/O redirection by checking PIOR2 bit = 1, and click on the button to fix the given pins as the pins to use.
- (2) Operation as a single master or as a slave is selected on the IICA0 tabbed page.

4. Workaround

Change the setting for the port 1 pins to that for the port 6 pins (P60/SCLA0 and P61/SDAA0) in the void R_IICA0_Create (void) function in the way shown below. The function is in the r_cg_serial.c file. These modifications are required every time code is generated.

Before modification:

```

-----
void R_IICA0_Create(void)
{
    .....
    /* Set SCLA0, SDAA0 pin */
    POM1 |= 0x30U;           /* Before modification */
    P1  &= 0xCFU;           /* Before modification */
    PM1 |= 0x30U;           /* Before modification */
    .....
    /* Set SCLA0, SDAA0 pin */
    PM1 &= 0xCFU;           /* Before modification */
}
-----

```

After modification:

```

-----
void R_IICA0_Create(void)
{
    .....
    /* Set SCLA0, SDAA0 pin */
    /* Delete the setting of POM1*/
    P6 &= 0xFCU;           /* Adjust from the setting for P1 */
    PM6 |= 0x03U;         /* Adjust from the setting for PM1 */
    .....
    /* Set SCLA0, SDAA0 pin */
    PM6 &= 0xFCU;         /* Adjust from the setting for PM1*/
}
-----

```

5. Schedule for Fixing the Problem

This problem will be fixed in the next version.

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