

RENESAS TOOL NEWS on March 16, 2006: RSO-E10A-USB-060316D

A Note on Using the Emulator Software Packages for the E10A-USB Emulators and an E200F Emulator

Please take note of the following problem in using the emulator software packages for the E10A-USB emulators and an E200F emulator, which are used for emulating the SuperH RISC engine family of MCUs:

- On erroneous descriptions in the I/O file for the SH7211F MCU, SH7211 group, SH7210 series

1. Products and Versions Concerned

- Emulator software V.2.03 Release 00
for the E10A-USB emulators (HS0005KCU01H and HS0005KCU02H)
- Emulator software V.2.01 Release 00
for the E200F emulator (R0E0200F1EMU00)

2. Descriptions

Erroneous descriptions have been found in the I/O file for the SH7211F (the SH7211.io file), which is included in the products concerned.

Since they cause errors to arise in the I/O Register window, please rectify them as follows:

(1) [UBC]

Delete the following four items:

BDR_0

BDMR_0

BDR_1

BDMR_1

(2) [Compare_Match_Timer]

Replace the six items shown in the 1st row of the

following table with the corresponding ones in the 2nd row:

CMCSR0 CMCSR_0
CMCNT0 CMCNT_0
CMCOR0 CMCOR_0
CMCSR1 CMCSR_1
CMCNT1 CMCNT_1
CMCOR1 CMCOR_1

(3) [Serial_Communication_Interface_with_FIF02]

Add the following item:

SCSEMR_2

(4) [A/D Converter]

Replace the addresses of the 17 registers shown in the 2nd row of the following table with the corresponding ones in the 3rd row:

Register	Address	Address

ADCR0	0xFFFFE400	0xFFFFE800
ADSR0	0xFFFFE402	0xFFFFE802
ADICER0	0xFFFFE404	0xFFFFE804
ADIEFR0	0xFFFFE406	0xFFFFE806
ADISR0	0xFFFFE408	0xFFFFE808
ADISTR0	0xFFFFE40A	0xFFFFE80A
ADIEIER0	0xFFFFE40C	0xFFFFE80C
ADSTRGR0	0xFFFFE41C	0xFFFFE81C
ADANSR0	0xFFFFE420	0xFFFFE820
ADDR0	0xFFFFE440	0xFFFFE840
ADDR1	0xFFFFE442	0xFFFFE802
ADDR2	0xFFFFE444	0xFFFFE804
ADDR3	0xFFFFE446	0xFFFFE806
ADDR4	0xFFFFE448	0xFFFFE808
ADDR5	0xFFFFE44A	0xFFFFE80A
ADDR6	0xFFFFE44C	0xFFFFE80C
ADDR7	0xFFFFE44E	0xFFFFE80E

Also delete the following two items:

ADCR1
ADSR1

(5) [Pin_Function_Controller]

Replace the address of the register shown in the 2nd row of the following table with the corresponding one in the 3rd row:

Register	Address	Address
IFCR	0xFFFE3A22	0xFFFE38A2

3. Workaround

To circumvent this problem, follow these steps:

(1) Download the corrected I/O file for the SH7211F (SH7211.io) (File size: 3.15 KB).

(2) Then overwrite the previous SH7211.io file with the corrected one.

The I/O file is saved in the IO Files folder, which is under the directory where any of the products concerned is installed.

4. Schedule of Fixing the Problem

We plan to fix this problem in the next release of the products.

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