# RENESAS Tool News

## A Note on Using Emulator Debuggers M3T-PD308MF, M3T-PD308F, M3T-PD30MF, and M3T-PD30F; and the M16C R8C Debugger Package

Please take note of the following problem in using the M3T-PD308MF, M3T-PD308F, M3T-PD30MF, and M3T-PD30F emulator debuggers and the M16C R8C debugger package:

- On making a change to the settings of the RAM monitoring area

1. **Products and Versions Concerned**
   - M3T-PD308MF V.1.00 Release 1 specific to the M30850T2-CPE compact emulator (for the M32C/84, M32C/85 and M32C/86 groups)

   - M3T-PD308F V.3.00 Release 1 through V.3.20 Release 1 (for the M32C/80 series)

   - M3T-PD30MF V.1.00 Release 1 specific to the M30290T2-CPE compact emulator (for the M16C/Tiny series)

   - M3T-PD30F V.2.00 Release 1 through V.2.20 Release 1 (for the M16C/60, M16C/30, and M16C/Tiny series)

   - M16C R8C debugger package V.1.00 Release 00 (for the M16C/60, M16C/30, M16C/Tiny, M16C/20, M16C/10, and R8C/Tinyseries)
   NOTE: Only when used in combination with the PC7501 or a compact emulator.

2. **Description**
   When a change is made to the settings of the RAM monitoring area using the RAM Monitor Area Setting window, the settings displayed on-screen may not match with the actual ones, resulting in improper operation.

   2.1 Conditions
       This problem occurs if either of the following operations is performed in the RAM

Monitor Area Setting window provided that the CPU clock is halted or the system clock is not supplied to the CPU:

(1) Making a change to the beginning address or block size of the specified RAM monitoring area.

(2) Deleting the specified RAM monitoring area by clicking the Remove or the Remove All button.

## 3. Workaround

Don't make any change to the settings of the RAM monitoring area while the CPU clock is halted or the system clock is not supplied to the CPU. If this problem arises, restart the debugger.

## 4. Schedule of Fixing the Problem

We plan to fix this problem in our next release of the products.