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A Note on Using the E200F and E10A-USB Emulators for the SH-2A Series of MCUs

Please take note of the problem shown below in using the E200F and E10A-USB Emulators, which are used for specific groups in the SuperH RISC engine family of MCUs.

1. Products Concerned

E200F and E10A-USB emulators

for the SH7206 group, SH7200 series, SuperH RISC engine family

Product Types:

Product	Product types
E200F	R0E0200F1EMU00
E10A-USB	HS0005KCU01H (Without AUD) HS0005KCU02H (With AUD)

2. Problem

If the event for a PC breakpoint takes place to halt the program, the value loaded in the R0 register may become incorrect.

3. Workaround

Don't use PC breakpoints but use hardware breakpoints as explained below. The hardware breakpoint can halt the program immediately before executing the instruction at the address at which a breakpoint is set, as well as the PC breakpoint.

In the E200F:

Use the "Prefetch address break before executing" condition of the On Chip Event hardware breakpoint.

In the E10A-USB:

Use the "Prefetch address break before executing" condition of the Event Condition hardware breakpoint.

To set the two types of hardware breakpoint mentioned above, double-click the On Chip Event column in the Editor or Disassemble window the same way as for the PC breakpoint.

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