

## A Note on Using C/C++ Compiler Package (M3T-CC32R) for M32R Family

When using the C/C++ compiler package for the M32R Family of MCUs, take note of the following problem:

- With using options for the MISRA C rule checker (SQMLint)
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### 1. Product Concerned

C/C++ compiler package for the M32R Family (M3T-CC32R)  
V.5.01 Release 01

### 2. Description

When the toolchain of the product concerned is used together with the MISRA C rule checker (SQMLint) on High-performance Embedded Workshop, no options for the SQMLint can be set, because "MISRA C rule check" is not displayed in the Category list box in the Renesas M32C Standard Toolchain dialog box of the High-performance Embedded Workshop.

To open the Renesas M32R Standard Toolchain dialog box, select the Renesas M32R Standard Toolchain command on the Build menu.

### 3. Workaround

To enter options for SQMLint, follow these steps:

- (1) On the Build menu, click Renesas M32R Standard Toolchain to open the Renesas M32R Standard Toolchain dialog box.
- (2) In the Categories list of the C/C++ tab, select Message to display "The User defined options" text box.
- (3) Enter the options you want to use into the txt box.

Example: -misra\_ignore "Rule No."

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