RENESAS Tool News

A Note on Using the C-Compiler Packages M3T-NC308WA and M3T-NC30WA

Please take note of the following problem in using the C compiler packages M3T-NC308WA and M3T-NC30WA (these are used for the M16C family of MCUs):

- On using the MISRA C rule checker SQMlint

1. **Products and Versions Concerned**
   M3T-NC308WA V.5.40 Release 00
   (for the M32C/90, /80, and M16C/80 series)

   M3T-NC30WA V.5.40 Release 00 and Release 00A
   (for the M16C/60, /30, /20, /10, /Tiny and R8C/Tiny series)

2. **Description**
   If the SQMlint is launched with the compile option -fdouble_32 (-fD32) * selected, the "Invalid argument" error arises, resulting in compilation being discontinued.

   * Interprets the size of a double-precision floating-point as 32 bits.

   NOTICES:
   1. When the following options are selected, the -fdouble_32 option becomes valid:
      - Option "-OR_MAX (-ORM)"
      - Option "-OS_MAX (-OSM)"

   2. When the following options are selected in the High-performance Embedded Workshop, also the -fdouble_32 option becomes valid:
      - Option "ROM size to the minimum"
      - Option "Speed to the maximum"

      How to select the above two options is as follows:
      Open the Build menu and select the Renesas M16C Standard Toolchain command. The

Renesas M16C Standard Toolchain dialog box appears.
In this dialog box, click the C tab and select Optimize in the Category list box. Then check the Size or speed check box and select "ROM size to the minimum" or "Speed to the maximum".
Pressing the OK button selects the option you want to use.

3. **Workaround**
   When using the SQMlint, make the -fdouble_32 (-fD32) option invalid.

4. **Schedule of Fixing the Problem**
   We plan to fix this problem in the next release of the products.