

Note on Using Automatic Driver Generator Applilet3 for RL78/D1A

When using automatic driver generator Applilet3 for RL78/D1A, take note of the following problem:

- With generating code that make setting of using Multiple PWM

1. Product Concerned

Applilet3 for RL78/D1A V1.00.01

2. Description

If the code is generated for making setting of using Multiple PWM on the TAU2 register in timer array unit 2, data is output on undefined channels in error.

Examples:

If you make the settings of Multiple PWMs and define channel 0 as master; and channels 1, 3, 5, and 7 as slaves in the TAU2 register, not channels 1, 3, 5, and 7, but channels 1, 2, 4, and 6 are enabled within the routine of the initializing function R_TAU2_Channel0_Start.

Incorrect code:

```
-----  
void R_TAU2_Channel0_Start(void)  
{  
.....  
    TOE2 |= _0002_TAU_CH1_OUTPUT_ENABLE |  
_0004_TAU_CH2_OUTPUT_ENABLE |  
        _0010_TAU_CH4_OUTPUT_ENABLE | _0040_TAU_CH6_OUTPUT_ENABLE;  
    TS2 |= _0001_TAU_CH0_START_TRG_ON | _0002_TAU_CH1_START_TRG_ON  
|  
        _0004_TAU_CH2_START_TRG_ON | _0010_TAU_CH4_START_TRG_ON |  
        _0040_TAU_CH6_START_TRG_ON;
```

```
}
```

Correct code:

```
-----  
void R_TAU2_Channel0_Start(void)  
{  
.....  
    TOE2 |= _0002_TAU_CH1_OUTPUT_ENABLE |  
_0008_TAU_CH3_OUTPUT_ENABLE |  
        _0020_TAU_CH5_OUTPUT_ENABLE | _0080_TAU_CH7_OUTPUT_ENABLE;  
    TS2 |= _0002_TAU_CH1_START_TRG_ON | _0008_TAU_CH3_START_TRG_ON  
|  
        _0020_TAU_CH5_START_TRG_ON | _0080_TAU_CH7_START_TRG_ON;  
}
```

3. Workaround

Edit the generated source code directly to rectify the incorrect channels to the correct ones.

4. Schedule of Fixing Problem

We plan to fix this problem in the next version of Applilet3 for RL78/D1A (to be published at the end of 2012).

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