RENESAS Tool News

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Note on Using Applilet3 for RL78

When using automatic driver generator Applilet3 for RL78, take note of the following problems:

- With selecting the 20-pin, 30-pin, or 32-pin package for the RL78/F13 or RL78/F14 group
- With using the remote control carrier wave mask signal in the RL78/L12 group
- With the case when ports that are not available in the MCU are displayed in the RL78/G14 group

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1. Problem with Selecting the 20-pin, 30-pin, or 32-pin Package for the RL78/F13 or RL78/F14 Group

1.1 Product Concerned

Applilet3 for RL78 V1.04.00

1.2 MCUs Involved

RL78/F13 and RL78/F14 groups of MCUs (RL78 family)

1.3 Description

When the 20-pin, 30-pin, or 32-pin package is selected for the RL78/F13 or RL78/F14 group and a divided frequency is selected for CPU and peripheral clock (fCLK) in the clock generator settings, the register settings are not output.

1.4 Workaround

To the clock setting function (void R_CGC_Create(void)) in the r_cg_cgc.c file, add the code for setting the registers for the case when a divided frequency is selected.
This should be added manually every time a code is generated.

Example: When 64 MHz is selected for PLL output
for main system clock and 32 MHz (fMP/2) is selected
for CPU and peripheral clock (fCLK)

```
------------------------------------------------------------------------
void R_CGC_Create(void)
{
```

```
. . . . . .
  /* Set fSL */
  SELLOSC = 1U;

  /* Set fCLK */
  CSS = 0U;         // Added: fMP is used as the clock for
                    //        the CPU and peripheral hardware.
  MDIV = 1;         // Added: Set to fMP/2.
. . . . . .
}
```

1.5 Schedule for Fixing the Problem

This problem will be fixed in the next version of Applilet3 for RL78.

2. Problem with Using the Remote Control Carrier Wave Mask Signal in the RL78/L12 Group

2.1 Product Concerned

Applilet3 for RL78 V1.04.00

2.2 MCUs Involved

RL78/L12 group of MCUs (RL78 family)

2.3 Description

There is an error in the R_TAU0_Channel2_Stop function for output when PWM output (remote control carrier wave mask signal) is selected in timer channel 2.

2.4 Workaround

In the line where a value is set for the TDO register value in the code output by void R_TAU0_Channel2_Stop(void) in the r_cg_timer.c file, correct each OR operator to an AND operator.
This should be corrected manually every time a code is generated.

Example:
Source code before modified

```
TO0 &= ~_0004_TAU_CH2_OUTPUT_VALUE_1 |
~_0008_TAU_CH3_OUTPUT_VALUE_1 |
      ~_0010_TAU_CH4_OUTPUT_VALUE_1 |
~_0020_TAU_CH5_OUTPUT_VALUE_1;
```

Source code after modified

```
  TO0 &= ~_0004_TAU_CH2_OUTPUT_VALUE_1 &
~_0008_TAU_CH3_OUTPUT_VALUE_1 &
      ~_0010_TAU_CH4_OUTPUT_VALUE_1 &
~_0020_TAU_CH5_OUTPUT_VALUE_1;
  ------------------------------------------------------------------
```

2.5 Schedule for Fixing the Problem

This problem will be fixed in the next version of Applilet3 for RL78.

3. Problem with the Case When Ports that Are Not Available in the MCU Are Displayed in the RL78/G14 Group

3.1 Product Concerned

Applilet3 for RL78 V1.04.00

3.2 MCUs Involved

RL78/G14 group of MCUs (RL78 family)

3.3 Description

When an RL78/G14 group MCU in the 80-pin package is selected, the settings for the P80 and P81 ports, which are not available in the selected MCU, are displayed.

3.4 Workaround

Do not make port settings for P80 and P81.

3.5 Schedule for Fixing the Problem

This problem will be fixed in the next version of Applilet3 for RL78.