RENESAS Tool News

Corrections to the Reference Manuals of Real-Time OSes M3T-MR32R

We notify corrections to the reference manuals of the M3T-MR32R real-time OSes for the M32R family MCUs as stated below.

1. **Documents Concerned**

   The reference manuals included with the M3T-MR32R V.3.00 Release 1 through V.3.50 Release 2

2. **Corrections**

   (1) Section 2.11.2, "ref_sys(Refer System Status)"
   In the TSS_LOC row of the Value column in the sysstat table, read 3 for 2

   (2) Section 3.6, "Common Constants and Packet Format of Structure"
   For the statements from the beginning of the typedef struct t_regs definition up to the end of the typedef struct t_exc one (before the comment "Related to Implementation-Dependent System Call (Mailbox)," read as follows:

   ```
   /* Register Information Packets
                   when forced exception has occurred */
   typedef struct t_regs {
       VW r0;   /* Contents of R0 register at that time */
       VW r1;   /* Contents of R1 register at that time */
       VW r2;   /* Contents of R2 register at that time */
       VW r3;   /* Contents of R3 register at that time */
       VW r4;   /* Contents of R4 register at that time */
       VW r5;   /* Contents of R5 register at that time */
       VW r6;   /* Contents of R6 register at that time */
       VW r7;   /* Contents of R7 register at that time */
       VW r8;   /* Contents of R8 register at that time */
       VW r9;   /* Contents of R9 register at that time */
       VW r10;  /* Contents of R10 register at that time */
   ```

```c
    VW r11;   /* Contents of R11 register at that time */
    VW r12;   /* Contents of R12 register at that time */
    VW r13;   /* Contents of R13 register at that time */
    VW r14;   /* Contents of R14 register at that time */
    VW sp;    /* Contents of SP register at that time */
    VW accl;  /* Contents of ACCL register at that time */
    VW acch;  /* Contents of ACCH register at that time */
    VW acc1l; /* Contents of ACC1L register at that time
                (the value of FPSR when kernel for FPU used;
                 indefinite except when kernels for FPU
                              and M32Rx/D used) */
    VW acc1h; /* Contents of ACC1H register at that time
                (indefinite except when kernel for M32Rx/D used) */
} T_REGS;

/* EIT Information Packets */
typedef struct t_reit {
    PSW psw;  /* Contents of PSW register at that time */
    FP pc;    /* Contents of PC at that time */
} T_EIT;

/* Exception Information Packets */
typedef struct t_exc {
    W exckind; /* kind of exception: always set to EXK_TER=2
                               (forced exception) */
    UW exccd;  /* exception code */
    ID tskid;  /* task ID */
    UW exeenv; /* system status where exception has occurred:
                    always set to TTE_TSK=0
                        (task is under execution) */
} T_EXC;
```