

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU	Document No.	TN-V85-A024A/E	Rev.	1.00
Title	V850E/Sx3-H Usage Restriction about bus control function.		Information Category	Usage Restriction	
Applicable Product	V850E/Sx3-H series	Lot No.	Reference Document	* V850E/SJ3-H,V850E/SK3-H User's Manual:Hardware Rev5.00 (the 5th edition) R01UH0247EJ0500	
		All lots			

A restriction matter of a bus control function was revealed about V850E/Sx3-H.

When using a bus control function of V850E/Sx3-H, the mentioning contents of the pin status which is at the time of idle state insertion by a multi-bus mode of plaques are corrected as follows.

1. Detail of restriction

When a bus control function of V850E/Sx3-H is used by a multi-bus mode, AD0-AD15 terminal becomes unsettled output under idle State._

User's manual are corrected as follows.

Item	Page	Correction point	Current state.	After restriction
1.	64.	Table 2-3 Pin Operation status in Various Modes.		<p>Caution 10. In multiplex mode, AD0-AD15 terminal will be unsettled output.</p>
2.	310.	(1) Bus cycle control register (BCC)	<p>Cautions 1. The internal ROM, internal RAM, and on-chip peripheral I/O areas are not subject to idle state insertion. 2. Write to the BCC register after reset, and then do not change the set values. Also, do not access an external memory area until the initial settings of the BCC register have been specified.</p>	<p>Cautions 1. The internal ROM, internal RAM, and on-chip peripheral I/O areas are not subject to idle state insertion. 2. Write to the BCC register after reset, and then do not change the set values. Also, do not access an external memory area until the initial settings of the BCC register have been specified. 3. In the idle state and AD0-AD15 terminal will be unsettled output.</p>
3.	314.	Fig 5-6. Multiplexed Bus Read Timing (Bus Size: 16 Bits, 16-Bit Access)		<p>Remark The broken lines indicate high impedance.</p>
4.	314.	Fig 5-7. Multiplexed Bus Read Timing (Bus Size: 8 Bits)		<p>Remark The broken lines indicate high impedance.</p>

2. Workaround

When using the bus function, be careful so that a signal of AD0-AD15 terminal doesn't collide with idle state.

This issue is not planned for correction. It will be made usage restriction.

Please inquire to our salesperson about details.