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## **RENESAS TECHNICAL UPDATE**

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Product Category	MPU/MCU	Document No.	TN-SH7-A780A/E	Rev.	1.00	
Title	Usage of periodic interrupt for Realtime Cloc	Information Category	Technical Notification			
Applicable Product	SH7262 Group SH7264 Group SH7266 Group SH7267 Group	Lot No.		Reference Document  - SH7262 Group, SH7264 Group Hardware Manual (REJ09B0445-0200) - SH7266 Group, SH7267 Group Hardware Manual (REJ09B0566-0100)		up
		ALL	Reference Document			up

We would like to inform you about the usage of periodic interrupt for Realtime Clock (RTC), included in the above-mentioned applicable product.

## [Error] Control Register 2 (RCR2)

6 to 4	PES[2:0]	000	R/W	Interrupt Enable Flags
				These bits specify the periodic interrupt.
				000: No periodic interrupts generated
				001: Periodic interrupt generated every 1/256 second
				(However, when EXTAL is selected (RCKSEL bit in RCR5 = 1), setting these bits to 001 generates periodic interrupt every 1/128 second.)
				010: Periodic interrupt generated every 1/64 second
				011: Periodic interrupt generated every 1/16 second
				100: Periodic interrupt generated every 1/4 second
				101: Periodic interrupt generated every 1/2 second
				110: Periodic interrupt generated every 1 second
				111: Periodic interrupt generated every 2 seconds

## [Correction] Control Register 2 (RCR2)

6 to 4	PES[2:0]	000	R/W	Interrupt Enable Flags
				These bits specify the periodic interrupt.
				000: No periodic interrupts generated
				001: Setting prohibited
				010: Periodic interrupt generated every 1/64 second
				011: Periodic interrupt generated every 1/16 second
				100: Periodic interrupt generated every 1/4 second
				101: Periodic interrupt generated every 1/2 second
				110: Periodic interrupt generated every 1 second
				111: Periodic interrupt generated every 2 seconds