To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	Microprocessor			No	TN-SH7-447A/E	Rev	1
THEME	Usage notices of the SH7709S/SH7729R/SH7709A/ SH7729 MCS0		Classification of Information	1. Spec change 2. Supplement of Documents ③ Limitation of Use 4. Change of Mask 5. Change of Production Line			
PRODUCT NAME	HD6417709S HD6417729R HD6417709A HD6417729	Lot No. ALL	Reference Documents	SH7709S Hardware manual Rev.3.0 ADE-602-250B SH7729R Hardware manual Rev.3.0 ADE-602-229B SH7709A Hardware manual Rev.5.0 ADE-602-187D SH7729 Hardware manual Rev.4.0 ADE-602-157C	Effective Date Eternity		

SH7709S/SH7729R/SH7709A/SH7729 MCS0 has following usage notes.

[Condition]
- Setting the PTC0/MCS0/PINT[0] pin as "other function (MCS0 function)"

and
- Setting CS2/0 bit in MCS0 control register (MCSCR0) "1"

[Phenomena]
On the above condition,
- The case accessing area 2 : CS0 is incorrectly asserted during the first CKIO cycle of MCS0 access cycle.
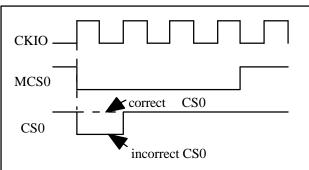

fig. 1. The case accessing area 2

- The case accessing area 0 : CS0 is asserted half CKIO cycle later than the correct timing.
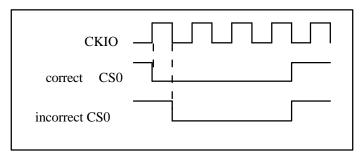

fig. 2. The case accessing area 0

[Workaround]

The followings are workaround.

- Don't use MCS0

- When using MCS0, please set CS2/0 bit in MCSCR0 "0".