To our customers,

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**Old Company Name in Catalogs and Other Documents**

On April 1\(^{st}\), 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: [http://www.renesas.com](http://www.renesas.com)

April 1\(^{st}\), 2010
Renesas Electronics Corporation

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Issued by: Renesas Electronics Corporation ([http://www.renesas.com](http://www.renesas.com))
HITACHI SEMICONDUCTOR TECHNICAL UPDATE

<table>
<thead>
<tr>
<th>Classification of Production</th>
<th>Microprocessor</th>
<th>No</th>
<th>TN-SH7-474A/E</th>
<th>Rev</th>
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<tbody>
<tr>
<td>PRODUCT NAME</td>
<td>HD6417709S</td>
<td>Lot No.</td>
<td>Reference Documents</td>
<td>Effective Date</td>
<td>Eternity</td>
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<td>HD6417729R</td>
<td>ALL</td>
<td>SH7709S Hardware manual Rev.3.0  ADE-602-250B</td>
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<td>HD6417706</td>
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<td>SH7729R Hardware manual Rev.3.0  ADE-602-229B</td>
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<td>SH7706 Hardware manual Rev.2.0  ADE-602-235A</td>
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SH7709S/SH7729R/SH7706 SDRAM bank active mode has following usage notes.

[Condition]
- Setting the RASD bit in Individual Memory Control Register(MCR)
  (Setting bank active mode)
  and
- Executing longword access to any area (area 0 to 6) or word/longword access to any area (area 0 to 6)

[Phenomena]
On the above condition, if the external bus access request signal conflicts with the auto-refresh request signal or self-refresh request signal in LSI, SDRAM all bank precharge which should be executed in the first cycle in the refresh cycles may not be executed and be changed to selected bank precharge.

[Workaround]
- Please use SDRAM auto precharge mode.
- When the bus width of each area is 32 bit, this malfunction is not occurred.