

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A842A/E	Rev.	1.00
Title	Usage notes about Direct Memory Access Controller (DMAC)		Information Category	Technical Notification		
Applicable Product	SH7709A Group SH7729 Group SH7706 Group SH7709S Group SH7729R Group SH7727 Group	Lot No.	Reference Document	SH7709A Group Hardware Manual Rev.5.00 (ADE-602-187D) SH7729 Group Hardware Manual Rev.4.00 (ADE-602-157C) SH7706 Group Hardware Manual Rev.5.00 (REJ09B0146-0500) SH7709S Group Hardware Manual Rev.5.00 (REJ09B0081-0500) SH7729R Group Hardware Manual Rev.5.00 (REJ09B0091-0500) SH7727 Group Hardware Manual Rev.6.00 (REJ09B0254-0600)		
		All				

We would like to inform you of the following usage notes about the Direct Memory Access Controller (DMAC).

When the DMA transfer started by the transfer request signal of an on-chip peripheral module is suspended by an NMI interrupt or an address error, please restart a DMA transfer in the following procedure.

- (a) Clear the DMAOR.NMIF bit, AE bit and DME bit to 0 once.
- (b) Set the DMAOR.DME bit to 1 again.