

NEC Microcomputer Technical Information

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<p style="text-align: center;">μPD789327 Subseries</p> <p style="text-align: center;">Document Modification</p>		Document No.	SBG-DT-0051-E	1/1
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Related documents	User's manual: U15043EJ1V0UM00 Preliminary product information: U14673EJ1V0PM00 U14411EJ1V0PM00	Notification classification		Usage restriction
				Upgrade
			√	Document modification
				Other notification

1. Affected products

μ PD789327 Subseries

μ PD789322, 789324, 789326, 789327

μ PD78F9328

2. Technical information

The following point will be modified in the above products.

The description In 4.3 Registers Controlling Port Function (3) Pull-up resistor option register B2 (PUB2) in the User's Manual (U15043EJ1V0UM00) and in 5.1.3 Port function control registers (3) Pull-up resistor option register B2 (PUB2) in the Preliminary Product Information (U14673EJ1V0PM00, U14411EJ1V0PM00)

(Before modification)

Pull-up resistor option register B2 (PUB2) sets whether on-chip pull-up resistors on P20 to P22 are used or not in bit units.

On the port specified to use an on-chip pull-up resistor by PUB2, the pull-up resistor can be internally used only for the bits set in the input mode. No on-chip pull-up resistors can be used for the bits set in the output mode regardless of the setting of PUB2. This also applies to cases when the pins are used for alternate functions.

(After modification)

Pull-up resistor option register B2 (PUB2) sets whether on-chip pull-up resistors on P20 to P22 are used or not in bit units.

On the port specified to use an on-chip pull-up resistor by PUB2, the pull-up resistor can be internally used regardless of the input/output mode setting. This also applies to cases when the pins are used for alternate functions.

3. Action

This modification will be included in the next revision of these documents.