Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

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Though this news contents have already been described in the manual and the data sheet of some

GRADE

No.M740-22-0012

MESC TECHNICAL NEWS

740 Family Notes on Interrupts

Classification

Concerned Products

Corrections and supplementary explnation of document

740 Family

✓ Notes Knowhow Others

When setting the followings, please follow the procedure of this news.

•When permitting serial I/O transmission interrupt

As for serial I/O that can be switched between a clock synchronous and asynchronous (UART) serial I/O, when setting the transmission enable bit to "1", the transmit interrupt request bit is automatically set to "1". When not requiring the interrupt occurrence synchronized with the transmission enabled, take the following sequence.

① Set the serial I/O transmit interrupt enable bit to "0" (disabled).

2 Set the transmit enable bit to "1".

③ Set the serial I/O transmit interrupt request bit to "0" after 1 or more instructions have been executed.

④ Set the serial I/O transmit interrupt enable bit to "1" (enabled).

•When setting external interrupt active edge

•When switching interrupt sources of an interrupt vector address where two or more interrupt sources are allocated

When setting these, the interrupt request bit may be set to "1". When not requiring the interrupt occurrence synchronized with these setting, take the following sequence.

① Set the corresponding interrupt enable bit to "0" (disabled).

⁽²⁾ Set the interrupt edge select bit (active edge switch bit) or the interrupt (source) select bit to "1".

③ Set the corresponding interrupt request bit to "0" after 1 or r ore instructions have been executed.

④ Set the corresponding interrupt enable bit to "1" (enabled).

products, we will inform you of this news as common notes for 740 Family.