Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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HITACHI MICROCOMPUTER TECHNICAL UPDATE

DATE	March 14th, 1997	No.	TN-EML-025A/E
THEME	Supporting H8/3035 Series with E7000 Emulator		
CLASSIFICATION	☐ Spec. change ☐ Limitation on Use ☐ Supplement of Documents		
PRODUCT NAME	HS3048EPD71H		TARGET LOTS
			All
REFERENCE DOCUMENT	H8/3048 Series, H8/3048F Emulator E7000 User's Manual (HS3048EPD70HE)	7000	Effective Date: Permanent
			From: March 14th, 1997

Details on the GAIN announcement of supporting the H8/3035 series with the E7000 emulator are described below.

The H8/3035 series cannot be supported by an E7000 emulator because ROM in the evaluation chip (microcomputer for evaluation mounted on the emulator) cannot be set to more than 128 kbytes. However, simple emulation of the H8/3035 series can be performed using the HS3048EPD71H E7000 emulator with the following procedure.

- 1. Select H8/3048 as the target device with the MODE command.
- 2. Set the operating mode according to table 1. (For the H8/3033, all of the following settings are unnecessary.)
- 3. Allocate standard emulation memory to the address range from H'20000 to H'3FFFF with the MAP command. If operating mode 3 is selected, cancel the access inhibition specification before allocating standard emulation memory.
- 4. Reset the emulator (enter the RESET command).
- 5. Modify the ASTCR value (I/O register; address H'FFFFED or H'FFFED) to a value in table 2, according to the operating mode.
- 6. Perform emulation with the GO or STEP command. (Note that the ASTCR value must always be modified after reset.)

Table 1

H8/3035 Series Operating Mode	Emulator Operating Mode	
1	5	
2	6	
3	7	

Table 2

H8/3035 Series Operating Mode	ASTCR Value	
1 or 3	H'FC	
2	H'FE	

- Notes: 1. When using the H8/3034, the address range from H'30000 to H'3FFFF cannot be specified as access-inhibited.
 - 2. In operating mode 2 of the H8/3034 or H8/3035, WAIT input to area 0 is disabled.
 - 3. Similar to the H8/3032 series, in operating modes 1 and 2, only data H'FF can be written to address H'FFFFEC or H'FFFEC.

The H8/3033 having 128-kbyte ROM will be added to the support lineup from July, 1997.