

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

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RENESAS TECHNICAL UPDATE

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|--------------------|--|----------|----------------------|-------------------------------------|------|------|
| Product Category | MPU/MCU | | Document No. | TN-SH7-A749A/E | Rev. | 1.00 |
| Title | SH7780 PCIC Master Access Parity Error Detection | | Information Category | Technical Notification | | |
| Applicable Product | SH7780 Group | Lot No. | Reference Document | SH7780 Hardware Manual (REJ09B0158) | | |
| | | All lots | | | | |

There is a notification about the SH7780 PCIC parity error detection in a master read access.

[Summary]

When the PCIC executes a master read access, the PCIC may not detect the parity error.

[Condition]

When all the following conditions (1)-(2) are satisfied, the PCIC can not detect a data parity error. And then the PERR# is not asserted, and PCIIR.MRDPEI is not set to 1 like the PCIC does not detect a data parity error.

(1)PCIC (master) condition

(1-1) PER bit in a PCI command register is set to 1. (Using parity error detection (PERR#))

(1-2) PCI transaction: memory read access (single or burst)

(2) External PCI device (target) condition

(2-1)Target initiated disconnect (with data): STOP# assert

[System problem]

When a target initiated disconnect (with data) occurs, the parity error for the disconnected data cycle **can** not be detected and the PERR# is not asserted.

[Workaround]

There is no workaround by the PCIC.

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