## Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

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## **RENESAS TECHNICAL UPDATE**

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Product Category	MPU&MCU				Document No.	TN-SH7-A662A/E	Rev.	1.00
Title	SH7780 PCIC Hardware Manual Correction				Information Category	Technical Notification		
				Lot No.				
Applicable Product	SH7780 Group			All lots	Reference Document	SH7780 Hardware Manual Rev.1.00 Dec.13.2005 (REJ09B0158-0100)		
Correction] PCIC Arbi	I line part is before c ter Bus Master Infor me and the descript	rmation Register (I	PCIBMIR	): SH7780 H	ardware Manua	l page 502, Section 13	.3.3(12)	
Note that, • Correction	# stands for low act	tive. to 0 bit name)			2	4		
Note that,	# stands for low act	tive. to 0 bit name)	4 Q4-3 ME	3 REQ <del>3</del> -2 BME	2 REQ <del>2</del> 1 BME	1 0 REQ <mark>1-0 REQ0-P</mark> BME BME		
Note that, • Correctio Bit:	# stands for low act	tive. to 0 bit name) 	4 Q4-3 ME	3 REQ <del>3</del> -2 BME	REQ <del>2</del> 1	REQ1-0 REQ0-P		
Note that, • Correctio Bit:	# stands for low act on of bit field (bits 4 15 –	tive. to 0 bit name) 	4 Q4-3 ME	3 REQ <del>3</del> -2 BME	REQ <del>2</del> 1	REQ1-0 REQ0-P		
Note that, • Correction Bit:	# stands for low act on of bit field (bits 4 15 	tive. to 0 bit name)  0 bit name and de Description REQ43 Error	4 Q4-3 ME escription	3 REQ <del>3-</del> 2 BME	REQ <del>2</del> 1 BME	REQ1-0 REQ0-P		
Note that, • Correction Bit: • Correction Bit	# stands for low act on of bit field (bits 4 15 - on of table (bits 4 to Bit Name REQ4-3	tive. to 0 bit name) RE( BI 0 bit name and de Description REQ43 Error An error occurs REQ <del>3</del> Error	4 Q4-3 ME escription	3 REQ <del>3-</del> 2 BME	REQ <del>2</del> 1 BME e 3 (REQ3#) fu	REQ <del>1</del> 0 REQ0 P BME BME	r.	
Note that, • Correction Bit: • Correction Bit 4	# stands for low act on of bit field (bits 4 15 - on of table (bits 4 to Bit Name REQ <del>4_</del> 3 BME	tive. to 0 bit name) REC BI 0 bit name and de Description REQ43 Error An error occurs REQ <del>3</del> 2 Error An error occurs REQ <del>3</del> 1 Error	4 Q4-3 ME escription when <del>the</del> when <del>the</del>	3 REQ <del>3-</del> 2 BME I) <del>PCIC</del> device <del>ovice 3 (REQ</del>	REQ <del>2</del> 1 BME ee 3 (REQ3#) fut <del>3#)</del> device 2 (RB	REQ <del>1</del> 0 REQ0 P BME BME	r. us master	
Note that, • Correction Bit: • Correction Bit 4 3	# stands for low act on of bit field (bits 4 15 - on of table (bits 4 to Bit Name REQ4-3 BME REQ2-2 BME	tive. to 0 bit name)  0 bit name and de Description REQ43 Error An error occurs REQ <del>2</del> 2 Error An error occurs REQ <del>2</del> 1 Error An error occurs REQ <del>2</del> 0 Error	4 Q4-3 ME escription when <del>the</del> when <del>de</del>	3 REQ <del>3-</del> 2 BME )) <del>9 PCIC</del> device <del>wice 3 (REQ</del>	REQ <del>2</del> 1 BME e 3 (REQ3#) fur <del>2#)</del> device 2 (RE <del>2#)</del> device 1 (RE	REQ4-0 BME BME	r. us master us master	
Note that, • Correction Bit: • Correction Bit 4 3 2	# stands for low act on of bit field (bits 4 15 - on of table (bits 4 to Bit Name REQ4-3 BME REQ2-2 BME REQ2-1 BME	tive. to 0 bit name) REC BI 0 bit name and de Description REQ43 Error An error occurs REQ2-1 Error An error occurs REQ4-0 Error An error occurs REQ4-0 Error	4 Q4-3 ME escription when <del>the</del> when <del>de</del> when <del>de</del> ror	3 REQ <del>3</del> -2 BME )) <del>&gt; PCIC</del> device <del>wice 3 (REQ</del> <del>wice 2 (REQ</del>	REQ <u>2</u> -1 BME e 3 (REQ3#) fut <del>2#)</del> device 2 (RE <del>2#)</del> device 1 (RE <del>1#)</del> device 0 (RE	REQ1-0 REQ0 P   BME BME   nctions as a bus master   EQ2#) functions as a b   EQ1#) functions as a b	r. us master us master us master	

