

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	MPU		No	TN-SH7-424A/E	Rev	1
THEME	SH7751/SH7751R MD pin status	Classification of Information	1. Spec change ② Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line			
PRODUCT NAME	SH7751, SH7751R	Lot No.	Reference Documents	SH7751 series Hardware manual Rev3.0 (ADE-602-201B)	Effective Date	
		ALL			Eternity	

There are notes about MD9 and MD10 pins in Appendix D Pin Functions in the SH7751 series hardware manual. Please refer the following tables.

Table D.2. Pin States in Reset, Power-Down State, and Bus-Released State (PCI Enable)

Pin name	I/O	Reset(Power on)	
		HOST	non HOST
PCIREQ2#/MD9	I/O	I *19	I *19
PCIREQ3#/MD10	I/O	I *19	I *19

*19: Pullup by on-chip pullup resistor. Note that this cannot be used for pullup of the mode pin during a power-on reset. Pullup or pulldown should be performed externally to the SH-4.

Table D.3. Pin States in Reset, Power-Down State, and Bus-Released State (PCI Disable)

Pin name	I/O	Reset(Power on)
PCIREQ2#/MD9	I/O	I *19
PCIREQ3#/MD10	I/O	I *19

*19: Pullup by on-chip pullup resistor. Note that this cannot be used for pullup of the mode pin during a power-on reset. Pullup or pulldown should be performed externally to the SH-4.