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Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU	Document No.	TN-SH7-A596A/E	Rev.	1.00
Title	SH7080 Group and SH7147 and SH7149: Error correction in the hardware manuals regarding the CK output in single-chip mode		Information Category	Technical Notification	
Applicable Product	All products of the SH7083, SH7084, SH7085, SH7086, SH7147, and SH7149	Lot No.	Reference Document	See [Applicable Documents] below.	
		All lots			

Thank you for your consistent patronage of Renesas semiconductor products.

We would like to inform you of error corrections in the description of the CK output function in single-chip mode in the SH7080 and SH7147Groups hardware manuals. Please take this information into consideration when using these products.

[Applicable Documents]

SH7080 Group Hardware Manual (REJ09B0181-0200, Rev. 2.0)

SH7146 Group Hardware Manual (REJ09B0229-0200, Rev. 2.0)

SH7147 Group Hardware Manual (REJ09B0230-0100, Rev. 1.0)

1. Revised Description (PFC)

In the section on the pin function controller (PFC) of the SH7080 and SH7147Groups hardware manuals, revise the description related to the CK output function in single-chip mode as follows (i.e. add note 3).

Port A Control Register L4 (PACRL4)

(1) SH7080 Group

Bit	Bit Name	Initial Value	R/W	Description
15	—	0	R	Reserved This bit is always read as 0. The write value should always be 0.
14	PA15MD2	0	R/W	PA15 Mode
13	PA15MD1	0	R/W	Select the function of the PA15/CK pin.
12	PA15MD0	0* ¹	R/W	000: PA15 I/O (port) 001: CK output (CPG)* ³ Other than above: Setting prohibited

(2) SH7147

Bit	Bit Name	Initial Value	R/W	Description
15	—	0	R	Reserved This bit is always read as 0. The write value should always be 0.
14	PA15MD2	0	R/W	PA15 Mode
13	PA15MD1	0	R/W	Select the function of the PA15/CK/SSO/TXD1 pin.
12	PA15MD0	0* ¹	R/W	000: PA15 I/O (port) 001: CK output (CPG)* ³ 101: SSO I/O (SSU) 110: TXD1 output (SCI) Other than above: Setting prohibited

(3) SH7149

Bit	Bit Name	Initial Value	R/W	Description
15	—	0	R	Reserved This bit is always read as 0. The write value should always be 0.
14	PA15MD2	0	R/W	PA15 Mode
13	PA15MD1	0	R/W	Select the function of the PA15/CK/TXD1 pin.
12	PA15MD0	0* ¹	R/W	000: PA15 I/O (port) 001: CK output (CPG)* ³ 110: TXD1 output (SCI) Other than above: Setting prohibited

Notes: 1. The initial value is 1 in the on-chip ROM enabled/disabled external-extension mode.

2. The initial value is 1 in the on-chip ROM disabled external-extension mode.

3. This setting is only valid in the on-chip ROM enabled/disabled external-extension mode. So, do not write 001 to these bits in the single-chip mode.

2. CK Output Function

The CK output function is only available in on-chip ROM enabled/disabled external-extension mode.

To use the CK output function, use the MCU in on-chip ROM enabled/disabled external-extension mode instead of single-chip mode.