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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A0071A/E	Rev.	1.00
Title	RZ/G2H, RZ/G2M V1.3, RZ/G2M V3.0, RZ/G2N and RZ/G2E Specification Change for Usage Notes		Information Category	Technical Notification		
Applicable Product	RZ/G Series, 2nd Generation RZ/G2H, G2M V1.3, G2M V3.0, G2N and G2E	Lot No. All lots	Reference Document	RZ/G Series, 2nd Generation User's Manual: Hardware Rev.1.00 (R01UH0808EJ0100)		

This technical update describes specification change of RZ/G Series, 2nd Generation product.

[Summary]

Specification change for “Hardware Electrical Characteristics Common to RZ/G Series, 2nd Generation products”.

[Priority level]

Importance: “Normal”

Urgency: “Normal”

[Products]

RZ/G2H

RZ/G2M V1.3

RZ/G2M V3.0

RZ/G2N

RZ/G2E

[Section number and title]

73.36 Usage Notes

[Correction]

1. Add new section 73.36 Usage Notes after Section 73.35, page 73-121

Current (from):

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Correction (to):

73.36 Usage Notes

RZ/G2H RZ/G2M V1.3 RZ/G2M V3.0 RZ/G2N RZ/G2E

Table 73.36.1 shows the usage notes of some I/O pins when using external pull-up resistor.

Table 73.36.1 Usage notes of I/O pins

I/O pin	Condition	Module Name	Product	Usage notes	Remark
3.3 V and 1.8 V	Use of external pull-up resistor at 1.8 V	SDHI	RZ/G2H, RZ/G2M V1.3, RZ/G2M V3.0, RZ/G2N	The following measure 1) or 2) shall be implemented. 1) VDD or VDDQ18 shall be turned on after the corresponding pin voltage, IOV is turned on. The combinations of power-on sequence are as follows; IOV -> VDD -> VDDQ18 IOV -> VDDQ18 -> VDD VDD -> IOV -> VDDQ18 VDDQ18 -> IOV -> VDD 2) External pullup resistor shall be less than or equal to 2.7 KΩ.	*1
			RZ/G2E	The above 2) shall be implemented.	*2
2.5 V	Use of external pull-up resistor at 2.5 V	Ethernet AVB-IF	RZ/G2H, RZ/G2M V1.3, RZ/G2M V3.0, RZ/G2N	External pull-up resistor shall be less than or equal to 9.3 KΩ.	—

Notes: 1. Make sure that POCCTR0 in PFC is changed when the corresponding pin voltage is used at 1.8 V.
2. Make sure that POCCTR0 to 2 in PFC are changed when the corresponding pin voltage is used at 1.8 V.

[Description]

Usage notes of some I/O pins when using external pull-up resistor.

[Reason for Correction]

Specification change

- End of Document -