

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A0091A/E	Rev.	1.00
Title	RZ/G2H, G2M V1.3, V3.0, G2N and G2E Document Corrections for VSP2		Information Category	Technical Notification		
Applicable Product	RZ/G Series, 2nd Generation	Lot No.	Reference Document	RZ/G Series, 2nd Generation User's Manual: Hardware Rev.1.10 (R01UH0808EJ0110)		
	RZ/G2H RZ/G2M V1.3, V3.0 RZ/G2N RZ/G2E	All lots				

This technical update describes document correction of RZ/G Series, 2nd Generation product.

[Summary]

Document correction for "RZ/G Series, 2nd Generation User's Manual: Hardware Rev.1.10".

[Priority level]

Importance: "Normal"

Urgency: "Normal"

[Products]

RZ/G2H

RZ/G2M V1.3, V3.0

RZ/G2N

RZ/G2E

[Section number and title]

Section 33. Video Signal Processor (VSP2)

“This is empty adjustment page to compare next Current (from) and Correction (to) on facing page. “

(By using two pages view of PDF readers this enables previously and prospectively view on odd and even pages.)

[Correction]

1. Section 33. VSP2, Page 33-139, 33.2.8.1 Concept of DPR Settings, In Example 1, two of Setting examples are wrong.

Current (from):

Table 33.26 VI6_DPR_*_ROUTE Register Settings in Connection Examples Shown in Figure 33.33

	Register Name	Setting
Example 0	VI6_DPR_RPF0_ROUTE.RT_RPF0	17 (To UDS)
	VI6_DPR_RPF1_ROUTE.RT_RPF1	63 (UNUSED)
	VI6_DPR_RPF2_ROUTE.RT_RPF2	63 (UNUSED)
	VI6_DPR_RPF3_ROUTE.RT_RPF3	63 (UNUSED)
	VI6_DPR_RPF4_ROUTE.RT_RPF4	63 (UNUSED)
	VI6_DPR_SRU_ROUTE.RT	63 (UNUSED)
	VI6_DPR_UDS_ROUTE.RT	46 (To SHP)
	VI6_DPR_UIF4_ROUTE.RT	63 (UNUSED)
	VI6_DPR_SHP_ROUTE.RT	56 (To WPF0)
	VI6_DPR_LUT_ROUTE.RT	63 (UNUSED)
	VI6_DPR_CLU_ROUTE.RT	63 (UNUSED)
	VI6_DPR_HST_ROUTE.RT	63 (UNUSED)
	VI6_DPR_HSI_ROUTE.RT	63 (UNUSED)
	VI6_DPR_ILV_BRS_ROUTE.RT	63 (UNUSED)
VI6_DPR_BRU_ROUTE.RT	63 (UNUSED)	
Example 1	VI6_DPR_RPF0_ROUTE.RT_RPF0	29 (To CLU)
	VI6_DPR_RPF1_ROUTE.RT_RPF1	24 (To BRUin1)
	VI6_DPR_RPF2_ROUTE.RT_RPF2	25 (To BRUin2)
	VI6_DPR_RPF3_ROUTE.RT_RPF3	26 (To BRUin3)
	VI6_DPR_RPF4_ROUTE.RT_RPF4	63 (UNUSED)
	VI6_DPR_SRU_ROUTE.RT	63 (UNUSED)
	VI6_DPR_UDS_ROUTE.RT	63 (UNUSED)
	VI6_DPR_UIF4_ROUTE.RT	63 (UNUSED)
	VI6_DPR_SHP_ROUTE.RT	63 (UNUSED)
	VI6_DPR_LUT_ROUTE.RT	56 (To WPF0)
	VI6_DPR_CLU_ROUTE.RT	22 (To LUT)
	VI6_DPR_HST_ROUTE.RT	63 (UNUSED)
	VI6_DPR_HSI_ROUTE.RT	63 (UNUSED)
	VI6_DPR_ILV_BRS_ROUTE.RT	63 (UNUSED)
VI6_DPR_BRU_ROUTE.RT	63 (UNUSED)	

Although both examples shown in figure 33.33 include operation in image processing modules, connect the RPF to the WPF directly when only image format conversion or packed format conversion is required.

Correction (to):

Table 33.26 VI6_DPR_*_ROUTE Register Settings in Connection Examples Shown in Figure 33.33

	Register Name	Setting
Example 0	VI6_DPR_RPF0_ROUTE.RT_RPF0	17 (To UDS)
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	VI6_DPR_RPF2_ROUTE.RT_RPF2	63 (UNUSED)
	VI6_DPR_RPF3_ROUTE.RT_RPF3	63 (UNUSED)
	VI6_DPR_RPF4_ROUTE.RT_RPF4	63 (UNUSED)
	VI6_DPR_SRU_ROUTE.RT	63 (UNUSED)
	VI6_DPR_UDS_ROUTE.RT	46 (To SHP)
	VI6_DPR_UIF4_ROUTE.RT	63 (UNUSED)
	VI6_DPR_SHP_ROUTE.RT	56 (To WPF0)
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	VI6_DPR_HST_ROUTE.RT	63 (UNUSED)
	VI6_DPR_HSI_ROUTE.RT	63 (UNUSED)
	VI6_DPR_ILV_BRS_ROUTE.RT	63 (UNUSED)
	VI6_DPR_BRU_ROUTE.RT	63 (UNUSED)
Example 1	VI6_DPR_RPF0_ROUTE.RT_RPF0	29 (To CLU)
	VI6_DPR_RPF1_ROUTE.RT_RPF1	24 (To BRUin1)
	VI6_DPR_RPF2_ROUTE.RT_RPF2	25 (To BRUin2)
	VI6_DPR_RPF3_ROUTE.RT_RPF3	26 (To BRUin3)
	VI6_DPR_RPF4_ROUTE.RT_RPF4	63 (UNUSED)
	VI6_DPR_SRU_ROUTE.RT	63 (UNUSED)
	VI6_DPR_UDS_ROUTE.RT	63 (UNUSED)
	VI6_DPR_UIF4_ROUTE.RT	63 (UNUSED)
	VI6_DPR_SHP_ROUTE.RT	63 (UNUSED)
	VI6_DPR_LUT_ROUTE.RT	23 (To BRUin0)
	VI6_DPR_CLU_ROUTE.RT	22 (To LUT)
	VI6_DPR_HST_ROUTE.RT	63 (UNUSED)
	VI6_DPR_HSI_ROUTE.RT	63 (UNUSED)
	VI6_DPR_ILV_BRS_ROUTE.RT	63 (UNUSED)
	VI6_DPR_BRU_ROUTE.RT	56 (To WPF0)

Although both examples shown in figure 33.33 include operation in image processing modules, connect the RPF to the WPF directly when only image format conversion or packed format conversion is required.

[Description]

Correction of setting example description.

[Reason for Correction]

General error correction.

- End of Document -