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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A0122A/E	Rev.	1.00
Title	RZ/G2H, G2M V1.3, G2M V3.0, G2N and G2E Addition of section 8 and section 9.Pin Function Controller (PFC)		Information Category	Technical Notification		
Applicable Product	RZ/G Series, 2nd Generation RZ/G2H RZ/G2M V1.3 RZ/G2M V3.0 RZ/G2N RZ/G2E	Lot No. All lots	Reference Document	RZ/G Series, 2nd Generation User's Manual: Hardware Rev.1.11 (R01UH0808EJ0111)		

This technical update describes document correction of RZ/G Series, 2nd Generation product.

[Summary]

Addition of section 8 and section 9.Pin Function Controller (PFC)

[Priority level]

Importance: “Normal”

Urgency: “Normal”

[Products]

RZ/G2H

RZ/G2M V1.3

RZ/G2M V3.0

RZ/G2N

RZ/G2E

[Section number and title]

Section 8. Pin Function Controller (PFC)

Section 9. Pin Function Controller (PFC) [RZ/G2E]

“This is empty adjustment page to compare next Current (from) and Correction (to) on facing page. “

(By using two pages view of PDF readers this enables previously and prospectively view on odd and even pages.)

[Correction]

1. Section 8. Pin Function Controller (PFC), Page 8-57, 8.4 Usage Note, 8.4.2 Notes for asserted PRESET# is added.

Current (from):

8.4 Usage Note

8.4.1 Notes for Using I2C0/3/5

When using the I2C0/3/5, the external connected devices must be 3.3-V compatible devices even though using the I2C0/3/5 system with 1.8-V.

If not using 3.3-V compatible devices, the external devices may be permanently damaged at a power-on reset.

Note.

1. The default functions of I2C0/3/5 pins after a power-on reset are GPIOs and pulled-up by 3.3-V through the VDDQ33 until they are specified for the I2C function or pulled-up off when other than I2C function is selected.

2. During the I2C0/3/5 pins are default state, i.e., GPIOs pins and pulled-up at 3.3-V, the voltage of external devices connected to these pins shall be up to 3.3-V through the VDDQ33.

Note that an internal leakage current of the I2C0/3/5 will be up to 55uA from the 3.3-V line to the 1.8-V line of each I2C block until the pulled-up off.

Correct (to):

8.4 Usage Note

8.4.1 Notes for Using I2C0/3/5

When using the I2C0/3/5, the external connected devices must be 3.3-V compatible devices even though using the I2C0/3/5 system with 1.8-V.
If not using 3.3-V compatible devices, the external devices may be permanently damaged at a power-on reset.
Note.
1. The default functions of I2C0/3/5 pins after a power-on reset are GPIOs and pulled-up by 3.3-V through the VDDQ33 until they are specified for the I2C function or pulled-up off when other than I2C function is selected.
2. During the I2C0/3/5 pins are default state, i.e., GPIOs pins and pulled-up at 3.3-V, the voltage of external devices connected to these pins shall be up to 3.3-V through the VDDQ33.
Note that an internal leakage current of the I2C0/3/5 will be up to 55uA from the 3.3-V line to the 1.8-V line of each I2C block until the pulled-up off.

8.4.2 Notes for asserted PRESET#

When PRESET# is asserted, a small glitch may occur at the pin configured to the peripheral output function.
Please be aware of this behavior if the connected device expects to continue the stable state at the PRESET# asserted timing.
If the timing of the PRESET# can be determined in advance, please set the pin to GPIO mode upfront of the PRESET# to avoid the situation.

[Description]

Added usage note.

[Reason for Correction]

Added usage note about the behavior of the GPIO pin during PRESET# is asserted.

[Correction]

2. Section 9. Pin Function Controller (PFC) [RZ/G2E], Page 9-38, 9.4 Usage Note, newly section is added.

Current (from):

— (None)

Correction (to):

9.4 Usage Note

9.4.1 Notes for asserted PRESET#

When PRESET# is asserted, a small glitch may occur at the pin configured to the peripheral output function.
Please be aware of this behavior if the connected device expects to continue the stable state at the PRESET# asserted timing.
If the timing of the PRESET# can be determined in advance, please set the pin to GPIO mode upfront of the PRESET# to avoid the situation.

[Description]

Added usage note.

[Reason for Correction]

Added usage note about the behavior of the GPIO pin during PRESET# is asserted.

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