

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A019A/E	Rev.	1.00
Title	RZ/G Series User's Manual: Hardware Corrections (manual errata)		Information Category	Technical Notification		
Applicable Product	RZ/G Series RZ/G1H, M, N, and E	Lot No.	Reference Document	RZ/G Series User's Manual: Hardware Rev.1.00		
		All lots				

This technical update describes document corrections for RZ/G Series.

[Summary]

RZ/G Series User's Manual: Hardware Corrections (manual errata)

[Products]

RZ/G1H, M, N and E (for details, refer to each correction)

[Note]

There is no specification change (document correction only).

[Corrections]

Gray highlighted parts (abcd) are corrected or newly added. And cancelation line parts (efgh) are deleted.

1. Section 21. Display Unit (DU) for RZ/G1H, M, N and E

- Table 21.16 External Synchronization Control Register Configuration (1) [Page 21-61]

Current (from):

DU1: H'FEB3 0000 (suffix 1)

Correction (to):

DU1: H'FEB3 1000 (suffix 1)

Note:

In the register description section, the correct base address of DU1 has been described. [Page 21-211]

2. Section 61. System Controller (SYSC)

- Section 61.2.1.13 External Event Request Status Enable Register2 (SYSCEERSER2) [Page 61-35]

Current (from):

Target products of SYSCEERSER2 register are H, M, N and E.

Correction (to):

Target products of SYSCEERSER2 register are **H and E**.

Note:

This register is intended only for products that are equipped with the Cortex-A7.

3. Section 31. Audio

- 31.1.6 Transfer Flow for RZ/G1H, M, N and E [Page 31-10 to 31-11]

It's necessary to correct all following description in this section.

Current (from):

transmitting (from the external device to SSI)

receiving (from SSI to the external device)

Correction (to):

transmitting (from **SSI** to **the external device**)

receiving (from **the external device** to **SSI**)

- End of document -