

RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RX*-A0271A/E	Rev.	1.00
Title	RX113 Group, Correction of Note Regarding the Specification of the Decoupling Capacitance for Stabilizing the Internal Voltage		Information Category	Technical Notification		
Applicable Product	RX113 Group	Lot No.	Reference Document	RX113 Group User's Manual: Hardware Rev.1.20 (R01UH0448EJ0120)		
		All				

This document describes a correction to a note on the decoupling capacitance required for stabilizing the internal voltage in RX113 Group User's Manual: Hardware, Rev.1.20.

•Page 1417 of 1491

The corrections are made as follows.

Before correction

Table 43.14 DC Characteristics (12)

Conditions: $1.8\text{ V} \leq V_{CC} = V_{CC_USB} \leq 3.6\text{ V}$, $1.8\text{ V} \leq AV_{CC0} \leq 3.6\text{ V}$, $V_{SS} = AV_{SS0} = V_{SS_USB} = 0\text{ V}$, $T_a = -40\text{ to }+105^\circ\text{C}$

Item	Symbol	Value
Decoupling capacitance to stabilize the internal voltage	C_{VCL}	$4.7\text{ }\mu\text{F } +50/-70\%^{*1}$

Note 1. Use a multilayer ceramic capacitor **whose** nominal capacitance **is** $4.7\text{ }\mu\text{F}$ **and** a capacitance tolerance **is** $\pm 30\%$ **or better**.

After correction

Table 43.14 DC Characteristics (12)

Item	Symbol	Value
Decoupling capacitance for stabilizing the internal voltage	C_{VCL}	$4.7\text{ }\mu\text{F } +50/-70\%^{*1}$

Note 1. Use a multilayer ceramic capacitor **with a** nominal capacitance **of** $4.7\text{ }\mu\text{F}$, **for which the sum of the** capacitance tolerance **and change in the capacitance under the usage conditions will be no greater than** $+50/-70\%$.