

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RH8-B0466A/E	Rev.	1.00
Title	RH850 C1M-A EMU3 issue		Information Category	Technical Notification		
Applicable Product	RH850 C1M-A1/C1M-A2	Lot No.	Reference Document	See related documents below.		
		All lot				

The limitations of EMU3 are added as follows:

Red character: Added

1. Modified Description

(1) C1M-A1/C1M-A2

The following description is added under Table 25.144 EMU3nNRECUk Register Contents (2/2)

Table 25.145 EMU3nNRECVk Register Contents (2/2) and Table 25.146 EMU3nNRECWk Register Contents (2/2).

CAUTION

When rewriting this register, be sure to follow steps (1) and (2) below.

This caution also applies to first-time writes.

(1) Write the following value in 32-bit access.

UMOD=0, UINT=0, UEN=0, UPTN=(set any value), UCMP=(set any value)

(2) UMOD, UINT, UEN, and UPTN settings should be written while retaining the values written in (1) of UCMP.

The following description is added under Table 25.145 EMU3nNRECVk Register Contents (2/2).

CAUTION

When rewriting this register, be sure to follow steps (1) and (2) below.

This caution also applies to first-time writes.

(1) Write the following value in 32-bit access.

VMOD=0, VINT=0, VEN=0, VPTN=(set any value), VCMP=(set any value)

(2) VMOD, VINT, VEN, and VPTN settings should be written while retaining the values written in (1) of VCMP.

The following description is added under Table 25.146 EMU3nNRECWk Register Contents (2/2).

CAUTION

When rewriting this register, be sure to follow steps (1) and (2) below.

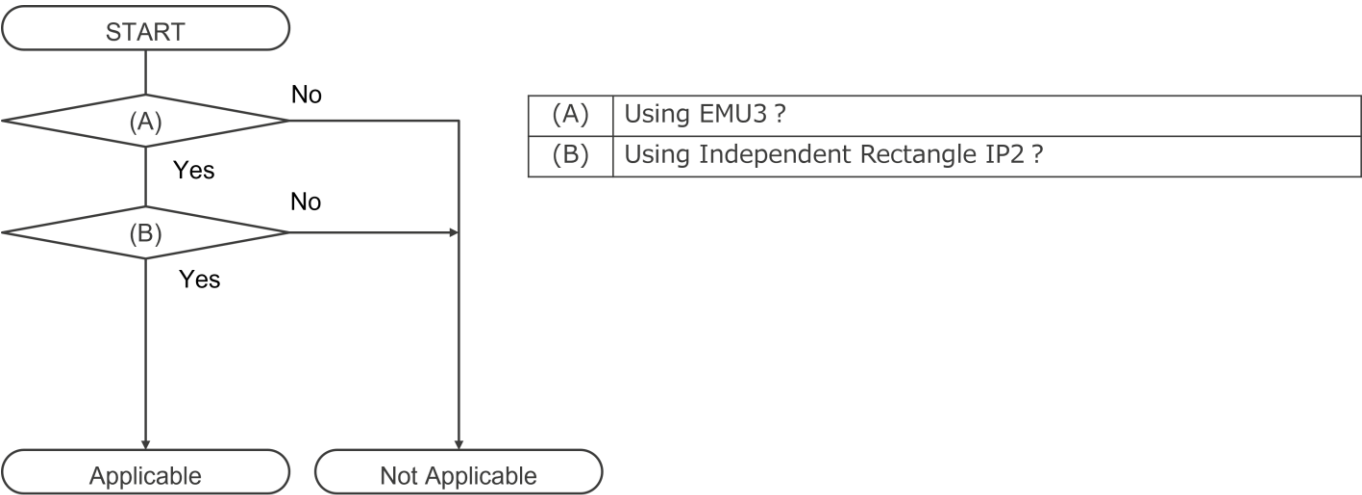
This caution also applies to first-time writes.

(1) Write the following value in 32-bit access.

WMOD=0, WINT=0, WEN=0, WPTN=(set any value), WCMP=(set any value)

(2) WMOD, WINT, WEN, and WPTN settings should be written while retaining the values written in (1) of WCMP.

2. Judgement flow



3. Reference Documents

Group	Document Title	Rev.	Document Number
C1M-A1/C1M-A2	RH850/C1M-A1, RH850/C1M-A2User's Manual: Hardware	1.20	R01UH0607EJ0120