

RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan
Renesas Electronics Corporation

Product Category	ASSP(Power Supply)		Document No.	TN-LIN-A002A/E	Rev.	1.00
Title	Regarding the revision of application note of R2A20114BFP		Information Category	Technical Notification		
Applicable Product	CCM interleave PFC IC R2A20114BFP	Lot No.	Reference Document	R2A20114BFP Application Note: R03AN0011EJ0100 Rev.1.00 2016.11.07		
		ALL lot				

For applicable product mentioned above, we will inform about the revision of application note.

1. Correction of writing error

In the explanation of the OFF function in P.10, when stopping the gate pulse with the OFF function, although it was a description which can be interpreted as automatically return to operation even when the OFF pin and the ERROR pin are directly connected via a logic inversion circuit, it does not automatically return to operation.

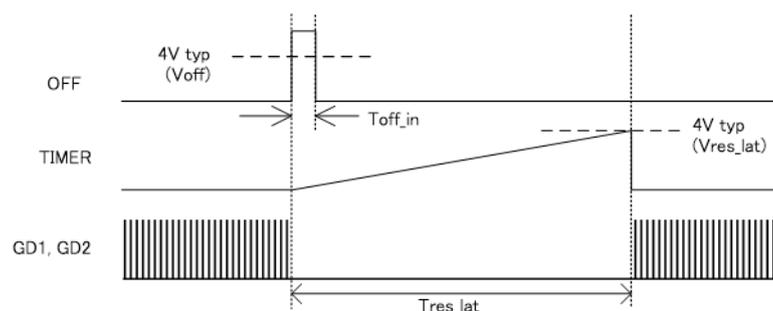
When the OFF pin and ERROR pin are directly connected via a logic inversion circuit, gate pulse stops by latching like the conventional product R2A20114AFP, and reset the latch by setting Vcc pin voltage to UVLO turn-off threshold or less.

OFF function description is corrected to the following contents.

When a pulse signal which amplitude is over than the threshold voltage V_{off} is added to OFF pin, GD pulse stops and TIMER pin is in charge mode. The OFF function is cancelled when TIMER pin is charged up to 4V(typ.) and the gate pulse is reset and the TIMER pin is discharged rapidly.

■ The pulse width T_{off_in} input to the OFF pin must be shorter than the TIMER pin charging time T_{res_lat} .

■ When stopping with the OFF function by error signal, since there is a possibility that each error signal is output during transient period such as startup, brownout, rapid load change, it is recommended to mask the error signal during the transient period, or count the number of error signal occurrence and stop the system when detecting a certain number of times by connecting to the OFF pin via a microcomputer or the like. When an error signal is received outside the masking period, follow the regulation of the above pulse for pulses input from the microcomputer to the OFF pin.



2. Additional note

Add the following contents to the explanation of soft start function and error function.

The error signal E_OVP may be output during the soft start period at startup. When the gate pulse is stopped by using E_OVP, it is recommended to mask the error signal, or count the number of error signal occurrence and stop the system when detecting a certain number of times by microcomputer or the like.