The description of Wake Up Interrupt Enable Register (WUPEN) is corrected.

**[Before]**

14.2.9 **Wake Up Interrupt Enable Register (WUPEN)**

Address(es): ICU.WUFEN 4000 6100h

The bits in this register control whether the associated interrupt can wake up the CPU from Software Standby mode.

**IRQWUPEN[15:0] bits (IRQ Interrupt Software Standby Returns Enable)**

The IRQWUPEN[15:0] bits enable the use of IRQn interrupts to cancel Software Standby mode. Only 0 to 4, 6, 7, 9, 11, 14, 15 bits can be used.
The bits in this register control whether the associated interrupt can wake up the CPU from Software Standby mode.

**IRQWUPEN[15:0] bits (IRQ Interrupt Software Standby Returns Enable)**

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