

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

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RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU		Document No.	TN-16C-A141A/E	Rev.	1.00
Title	R8C/14, 15, 16, 17 Groups Precautions on EW1 Mode of CPU Rewrite Mode		Information Category	Technical Notification		
Applicable Product	R8C/14, 15, 16, 17 Groups	Lot No.	Reference Document			
		All				

1. Precautions

When an interrupt request is generated during erasing with FMR01=1 (CPU rewrite mode enabled), FMR11=1 (EW1 mode) and FMR40=0 (disable erase suspend function), a CPU may not operate properly.

2. Cause

When an interrupt request is generated during erasing with FMR01=1 (CPU rewrite mode enabled), FMR11=1 (EW1 mode) and FMR40=0 (disable erase suspend function), the CPU has to move into the interrupt process with the fixed ROM address. Since addresses other than a certain specific ROM address cannot be read, the CPU runs out of control.

3. Software Countermeasures

Select any of the following 3 processes as a software countermeasure:

- (1) Disable an interrupt by setting the priority level of all maskable interrupts to "0". Note that disabling the interrupts by the I flag will not be in the software countermeasure.
- (2) Set the FMR40=1 (enable erase suspend function) when using the FMR11=1 (EW1 mode).
- (3) Use EW0 mode.